



GP
ELECTRONICS

DTC143EM

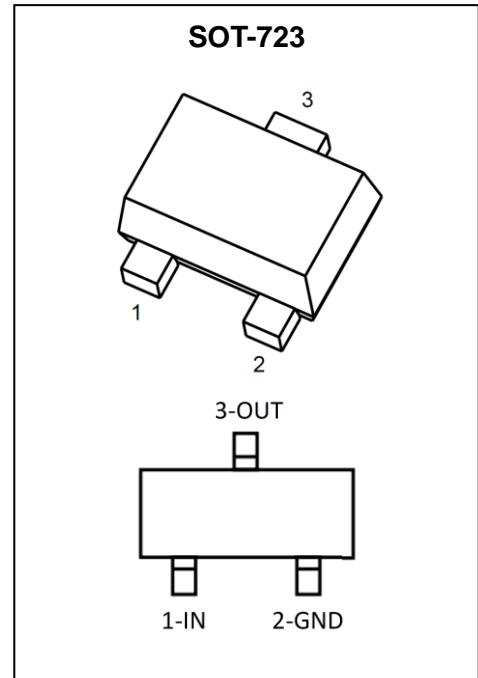
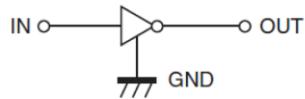
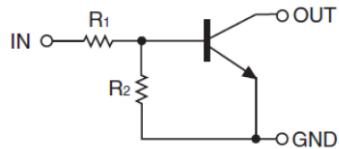
Digital Transistor

DTC143EM Digital Transistor(NPN)

Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input .They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

Schematic diagram



Marking : 23

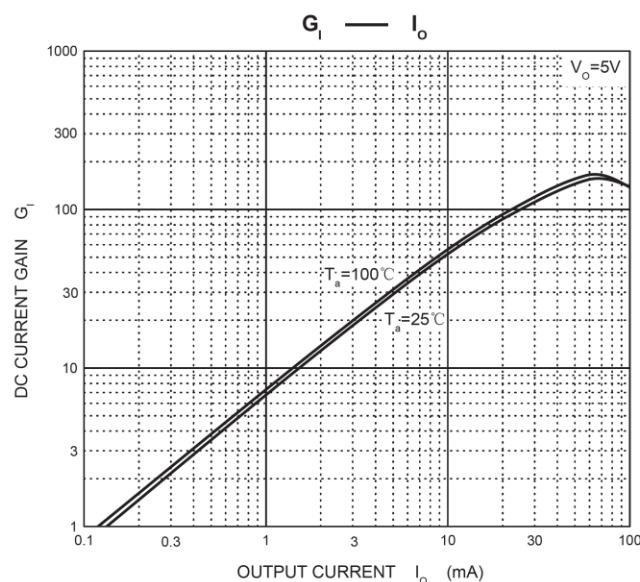
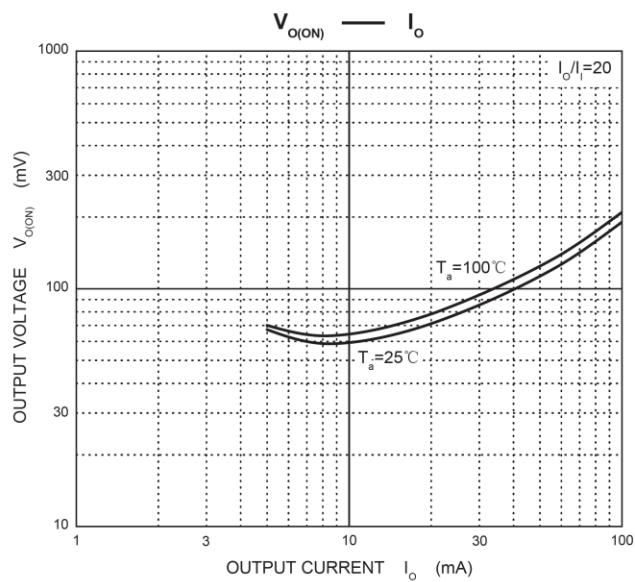
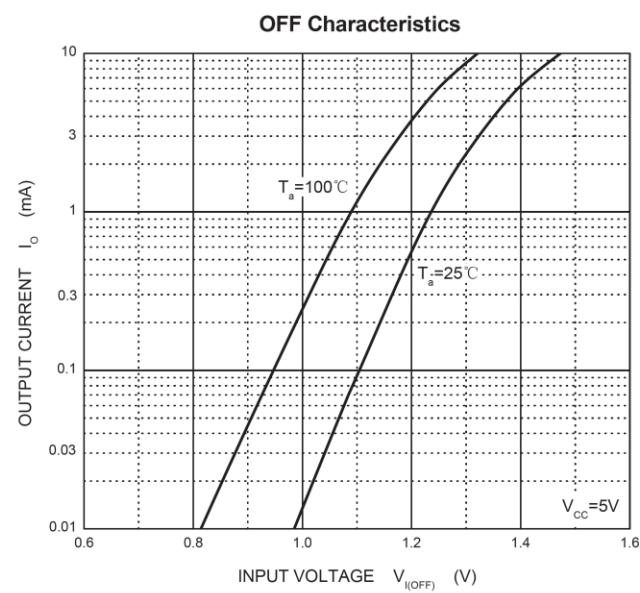
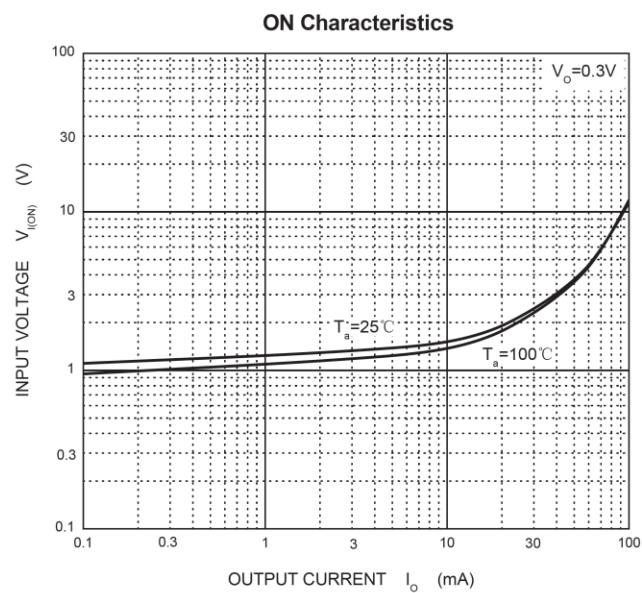
ABSOLUTE MAXIMUM RATINGS (T_a=25°C unless otherwise noted)

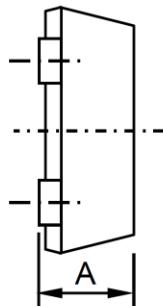
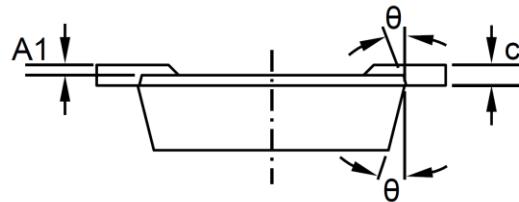
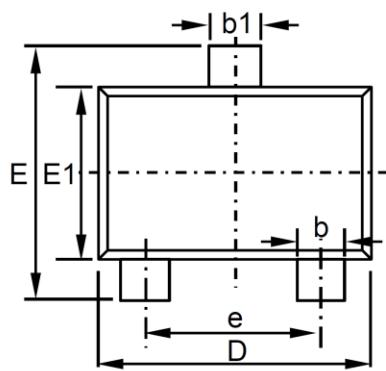
Parameter	Symbol	Value	Unit
Supply Voltage	V _{CC}	50	V
Input Voltage	V _{IN}	-10~+30	V
Output Current	I _O	100	mA
Power Dissipation	P _D	150	mW
Junction Temperature	T _J	125	°C
Storage Temperature Range	T _{STG}	-45 ~ +125	°C

ELECTRICAL CHARACTERISTICS (T_a=25°C unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Input voltage	V _{I(off)}	V _{CC} =5V , I _O =100μA	0.5			V
	V _{I(on)}	V _O =0.3V , I _O =20mA			3	V
Output voltage	V _{O(on)}	I _O =10mA , I _I =0.5mA			0.3	V
Input current	I _I	V _I =5V			1.8	mA
Output current	I _{O(off)}	V _{CC} =50V , V _I =0V			0.5	μA
DC current gain	G _I	V _O =5V , I _O =10mA	20			
Input resistance	R _I		3.29	4.7	6.11	kΩ
Resistance ratio	R ₂ / R ₁		0.8	1	1.2	
Transition frequency	f _T	V _O =10V,I _O =5mA,f=1MHz		250		MHz

Typical Characteristics



SOT-723 Package Information


SOT-723 (unit: mm)		
Dim.	Min.	Max.
A	0.40	0.50
A1	0.00	0.05
b	0.15	0.27
b1	0.20	0.37
c	0.06	0.16
D	1.10	1.30
E	1.10	1.30
E1	0.70	0.90
e	0.80 TYP.	
θ	7° REF.	