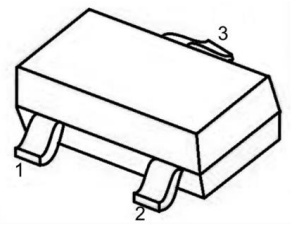
DTC113ZUA

Digital Transistor

DTC113ZUA Digital Transistor(NPN)

SOT-323



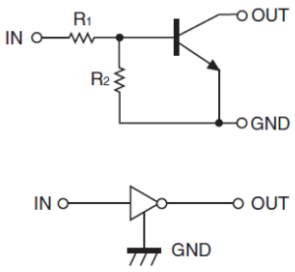
1.IN 2.GND 3.OUT

Feature

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input .They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

Marking： E21

Schematic diagram



ABSOLUTE MAXIMUM RATINGS (T_a=25℃ unless otherwise noted)

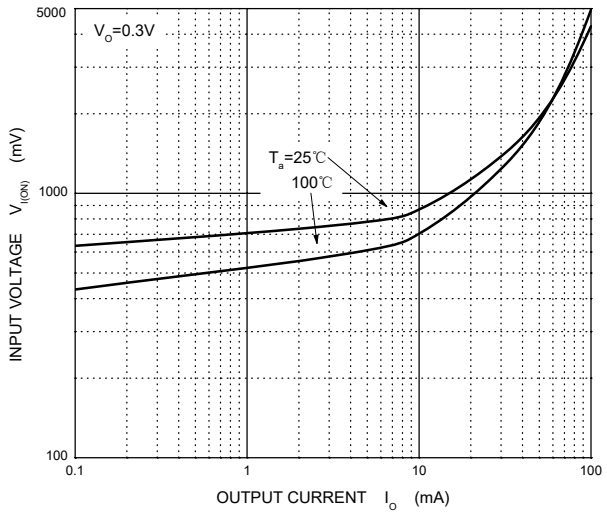
Parameter	Symbol	Value	Unit
Supply Voltage	V_{CC}	50	V
Input Voltage	V_{IN}	-5~+10	V
Output Current	I_O	100	mA
Power Dissipation	P_D	200	mW
Junction Temperature	T_J	125	℃
Storage Temperature Range	T_{STG}	-55 ~ +150	℃

ELECTRICAL CHARACTERISTICS (T_a=25℃ unless otherwise noted)

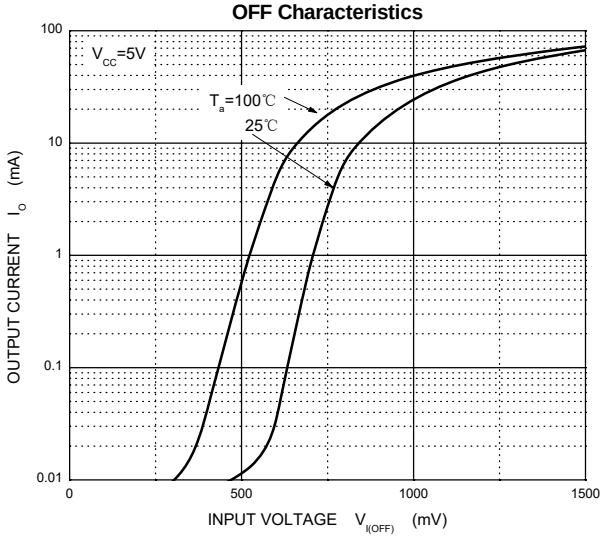
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Input voltage	$V_{I(off)}$	V_{CC}=5V , I_O=100μA	0.3			V
	$V_{I(on)}$	V_O=0.3V , I_O=20mA			3	V
Output voltage	$V_{O(on)}$	I_O=10mA ,I_I=0.5mA			0.3	V
Input current	I_I	V_I =5V			7.2	mA
Output current	$I_{O(off)}$	V_{CC}=50V , V_I=0V			0.5	μA
DC current gain	G_I	V_O=5V ,I_O =5mA	33			
Input resistance	R_1		0.7	1.0	1.3	kΩ
Resistance ratio	R_2/ R_1		8	10	12	
Transition frequency	f_T	V_O=10V,I_O=5mA,f=100MHz		250		MHz

Typical Characteristics

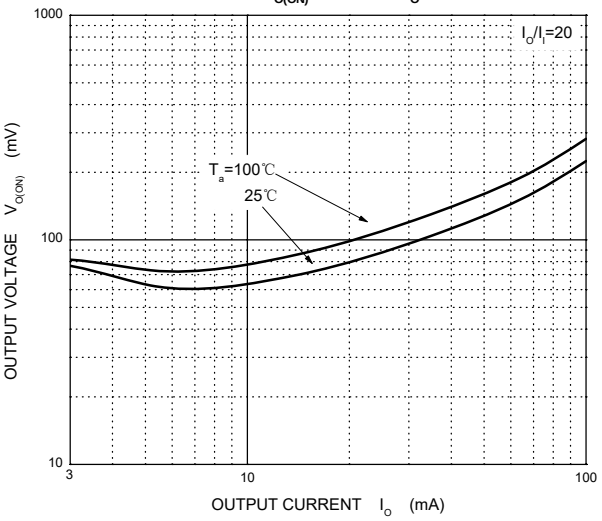
ON Characteristics



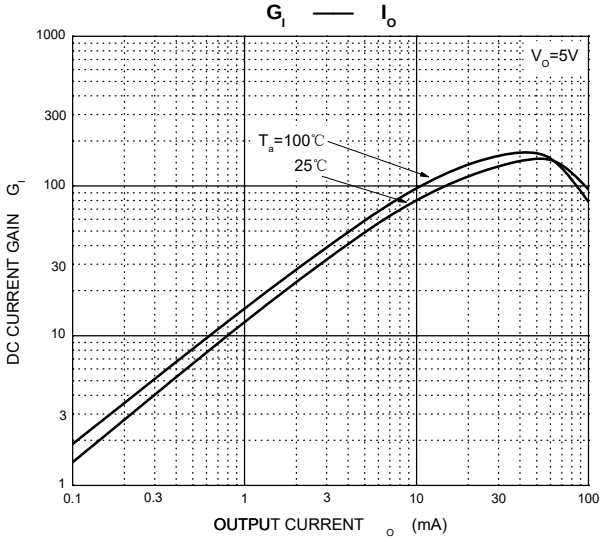
OFF Characteristics



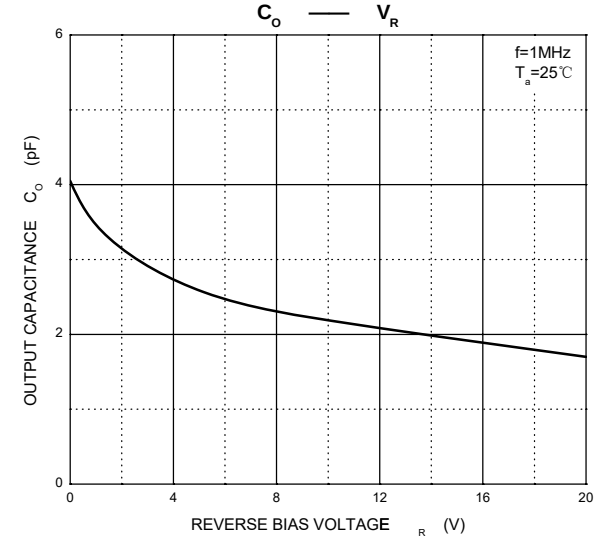
$V_{O(ON)}$ — I_O



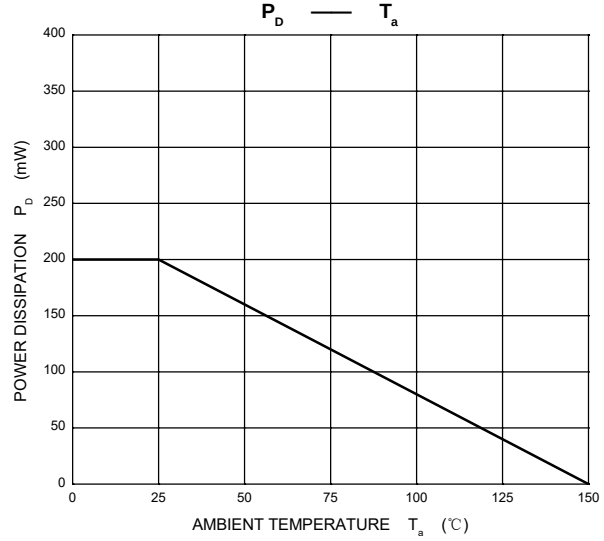
G_I — I_O



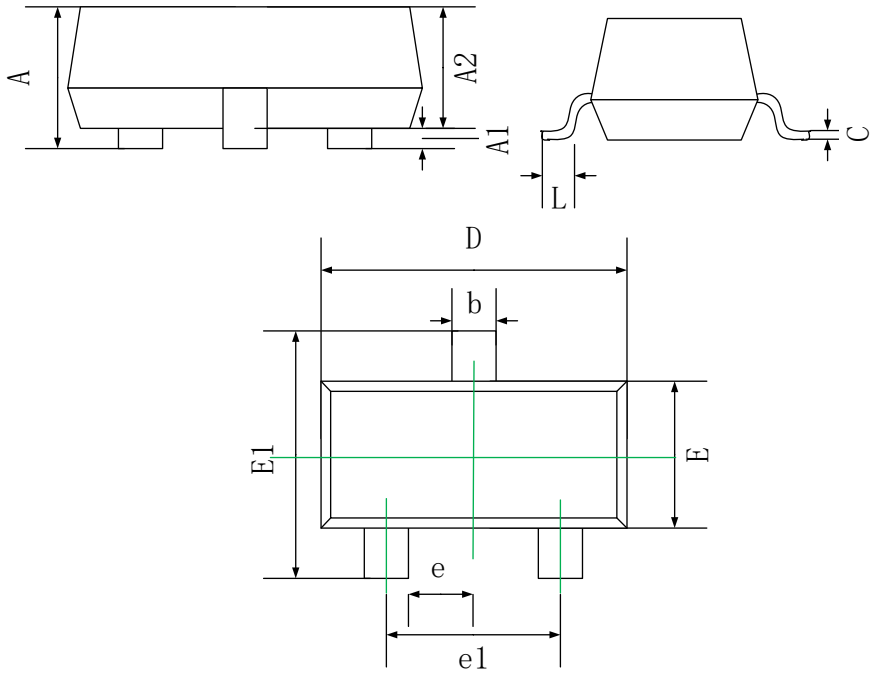
C_O — V_R



P_D — T_a

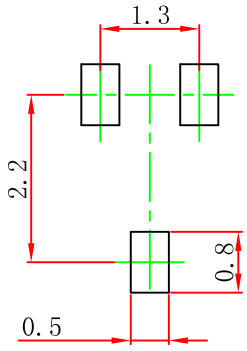


SOT-323 Package Information



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	0.70	0.90
A1	0.00	0.10
A2	0.70	0.80
b	0.25	0.35
c	0.10	0.20
D	1.50	1.70
E	0.70	0.90
E1	1.45	1.75
e	0.50 TYP.	
e1	0.90	1.10
L	0.40 REF.	
L1	0.26	0.46

SOT-323 Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.