

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
30V	5.7mΩ@10V	47A
	8.8mΩ@4.5V	

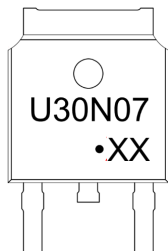
Feature

- Trench Technology Power MOSFET
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Application

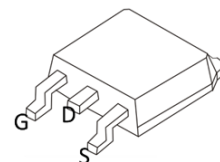
- Power Switching Application

MARKING:

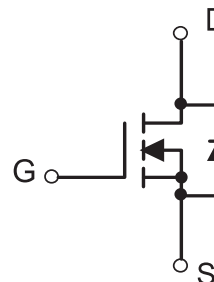


U30N07 = Device Code
 XX = Date Code
 Solid Dot = Green Indicator

TO-252-2L



Schematic diagram



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain - Source Voltage		V_{DS}	30	V
Gate - Source Voltage		V_{GS}	±20	V
Continuous Drain Current ¹	$T_C = 25^\circ\text{C}$	I_D	47	A
Pulsed Drain Current ²		I_{DM}	188	A
Single Pulsed Avalanche Current ³		I_{AS}	33	A
Single Pulsed Avalanche Energy ³		E_{AS}	54.5	mJ
Power Dissipation ⁵	$T_C = 25^\circ\text{C}$	P_D	31.2	W
Power Dissipation ⁶	$T_A = 25^\circ\text{C}$	P_D	2.5	W
Thermal Resistance from Junction to Ambient ⁶		$R_{\theta JA}$	50	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case		$R_{\theta JC}$	4	$^\circ\text{C/W}$
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55~ +150	$^\circ\text{C}$

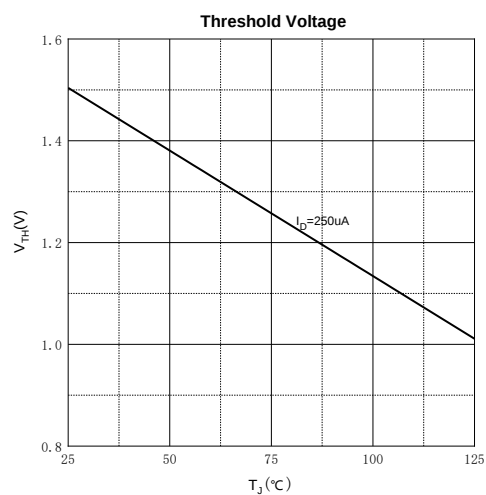
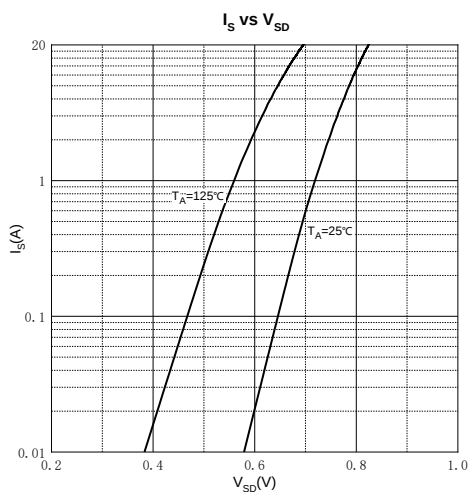
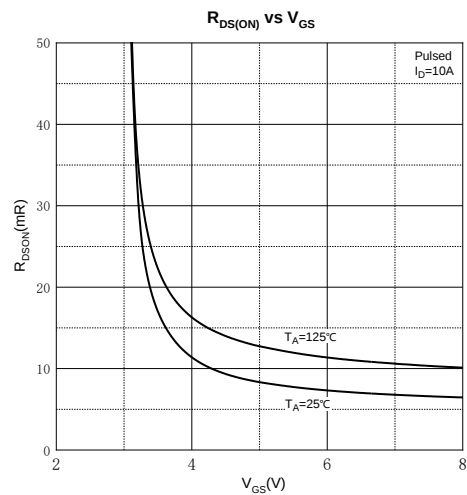
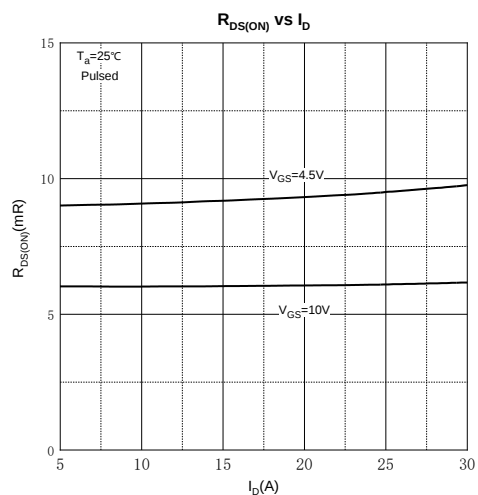
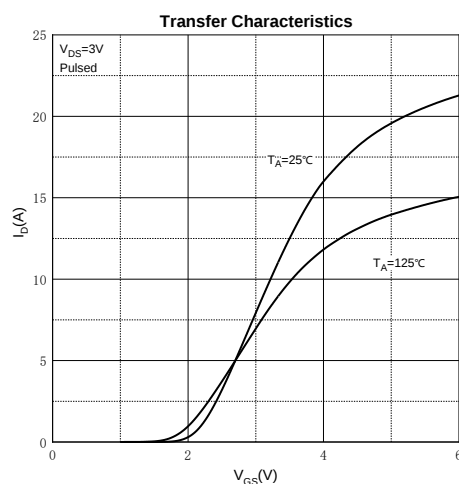
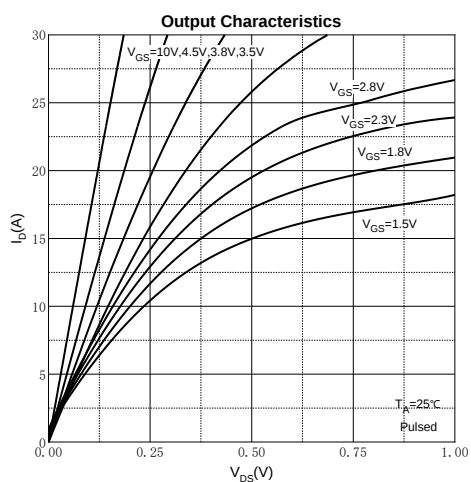
MOSFET ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

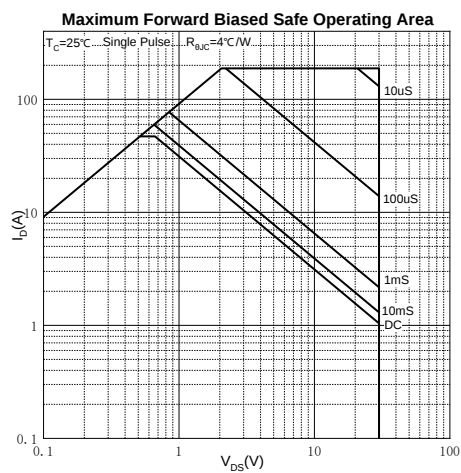
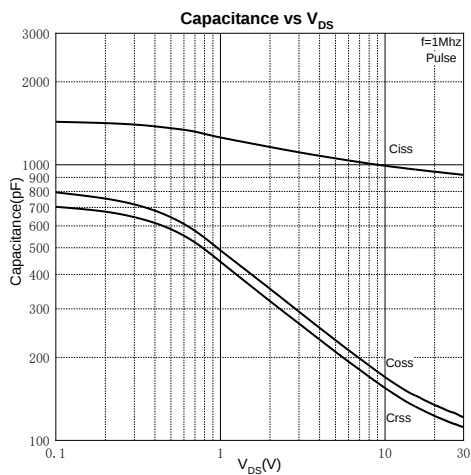
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 24V, V _{GS} = 0V			1	μA
Gate - Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
On Characteristics ⁴						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1.0	1.5	3.0	V
Drain-source On-resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 20A		5.7	7.3	mΩ
		V _{GS} = 4.5V, I _D = 20A		8.8	13	
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} = 15V, V _{GS} = 0V, f = 1MHz		990.2		pF
Output Capacitance	C _{oss}			143.7		
Reverse Transfer Capacitance	C _{rss}			128.2		
Gate Resistance	R _g	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz		1.95		Ω
Switching Characteristics						
Total Gate Charge	Q _g	V _{DS} = 15V, V _{GS} = 4.5V, I _D = 20A		22.2		nC
Gate-source Charge	Q _{gs}			3.0		
Gate-drain Charge	Q _{gd}			4.3		
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15V, V _{GS} = 10V, R _L = 0.75Ω R _G = 3Ω		6.5		ns
Turn-on Rise Time	t _r			2		
Turn-off Delay Ttime	t _{d(off)}			17		
Turn-off Fall Time	t _f			3.5		
Source - Drain Diode Characteristics						
Diode Forward Voltage ⁴	V _{SD}	V _{GS} = 0V, I _S = 10A			1.2	V

Notes :

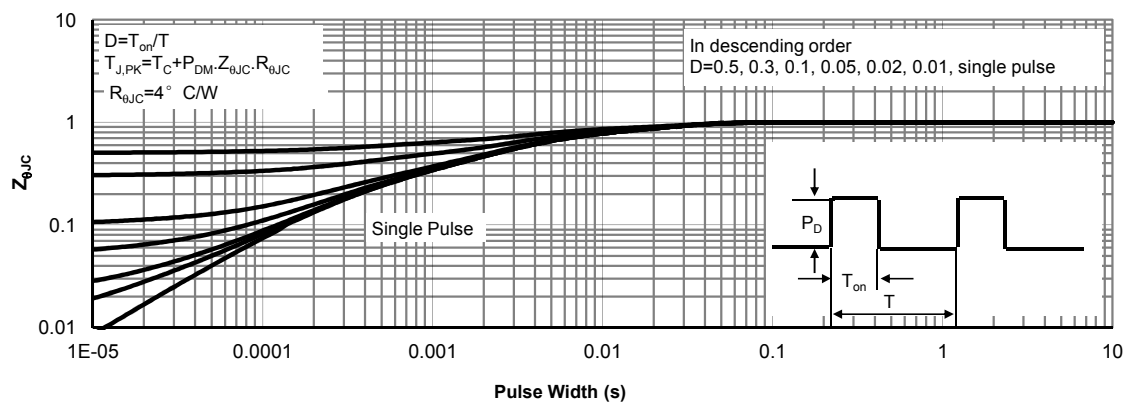
- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- 3.EAS condition: $V_{DD} = 15V, V_{GS} = 10V, L = 0.1mH, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
- 4.Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- 5.The power dissipation P_D is limited by $T_{J(MAX)} = 150^\circ\text{C}$.And device mounted on a large heatsink
- 6.Device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

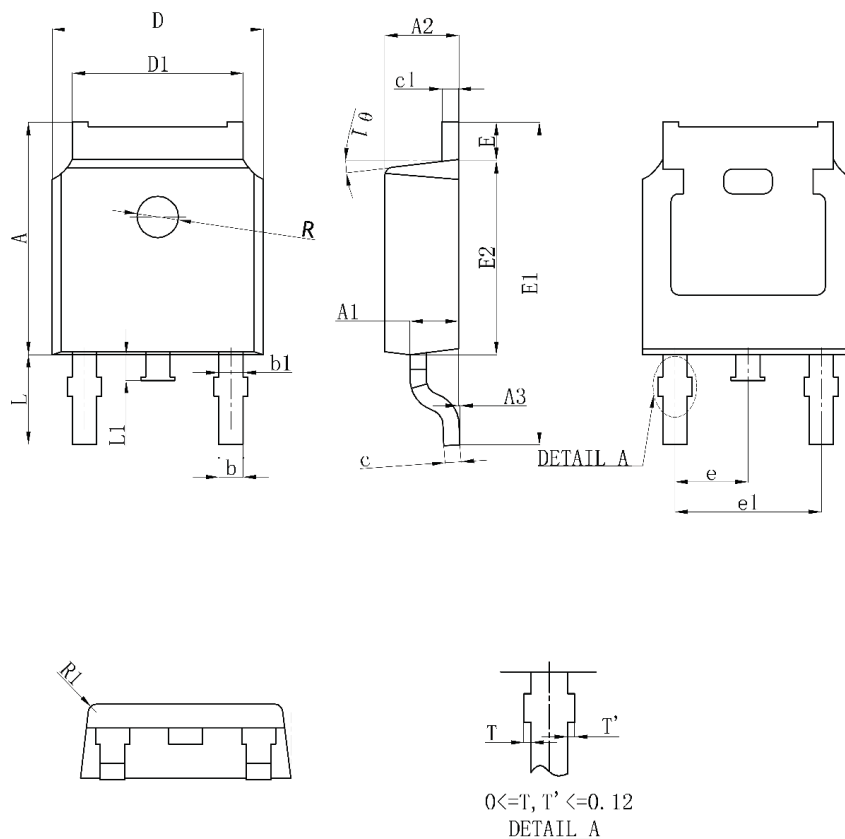
Typical Characteristics





Normalized Maximum Transient Thermal Impedance



TO-252-2L Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	7.050	7.150	0.278	0.281
A1	0.960	1.060	0.038	0.042
A2	2.200	2.400	0.087	0.094
A3	0.000	0.100	0.000	0.004
b	0.760REF		0.030REF	
b1	1.000REF		0.039REF	
c	0.508REF		0.020REF	
c1	0.508REF		0.020REF	
D	6.550	6.650	0.258	0.262
D1	5.100	5.460	0.201	0.215
E	0.950	1.050	0.037	0.041
E1	9.700	10.400	0.382	0.409
E2	6.000	6.200	0.236	0.244
e	2.286BSC		0.090BSC	
e1	4.572REF		0.180REF	
L	2.650	2.950	0.104	0.116
L1	0.700	0.900	0.028	0.035
θ	7°REF		7°REF	
R	1.300REF		0.051REF	
R1	0.250REF		0.010REF	