



GP
ELECTRONICS

GPMN4008

40V N-Channel MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
40V	15mΩ@10V	15A
	20mΩ@4.5V	

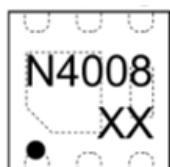
Feature

- High density cell design for ultra low $R_{DS(ON)}$
- Excellent package for good heat dissipation

Application

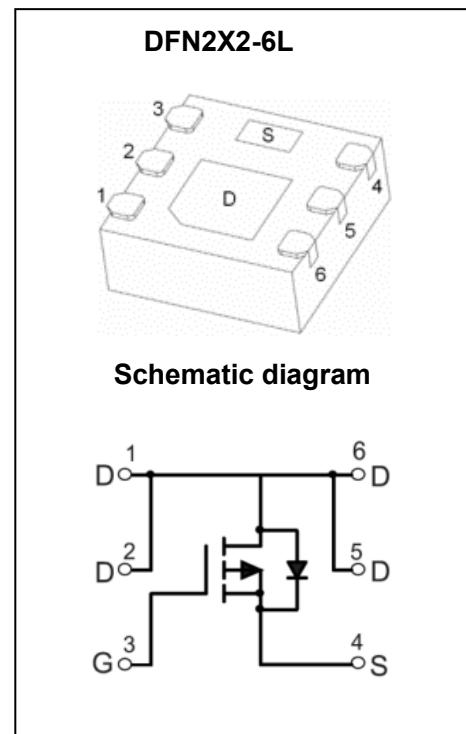
- Power switching application
- Hard switched and high frequency circuits
- Uninterruptible power supply

MARKING:



N4008 = Device Code

XX = Date Code



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	I_D	15	A
		12	A
Pulsed Drain Current	I_{DM}	60	A
Single Pulsed Avalanche Energy	E_{AS}^*	55	mJ
Power Dissipation	P_D	1.8	W
Thermal Resistance from Junction to Ambient ¹	$R_{\theta JA}$	41	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

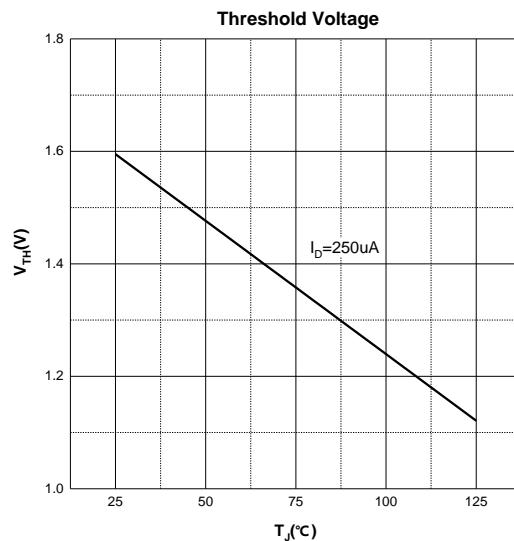
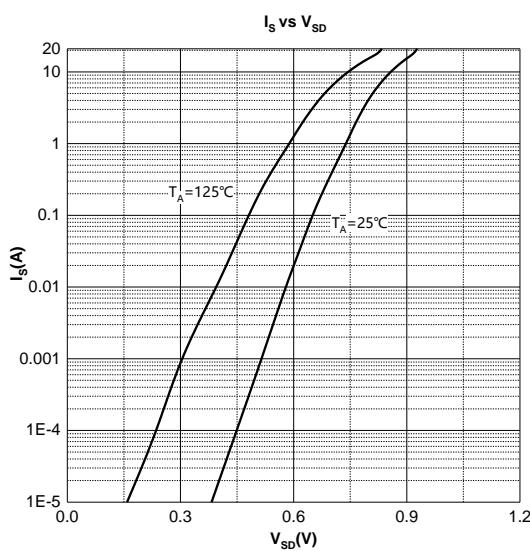
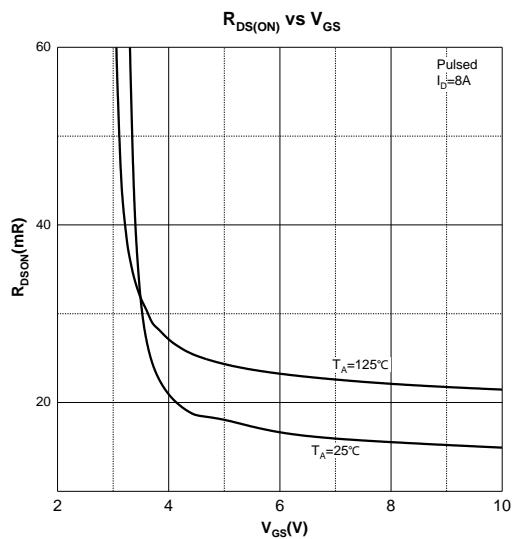
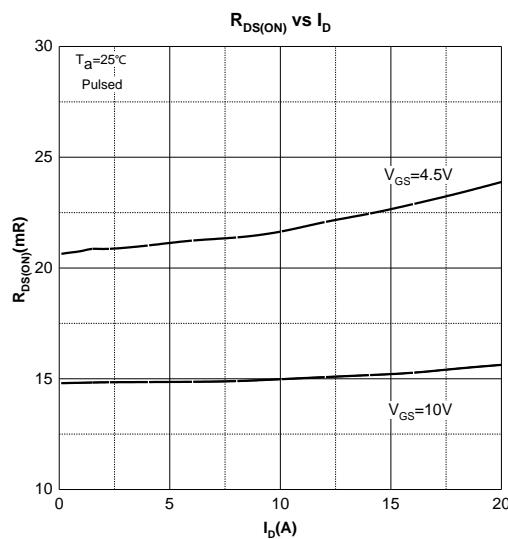
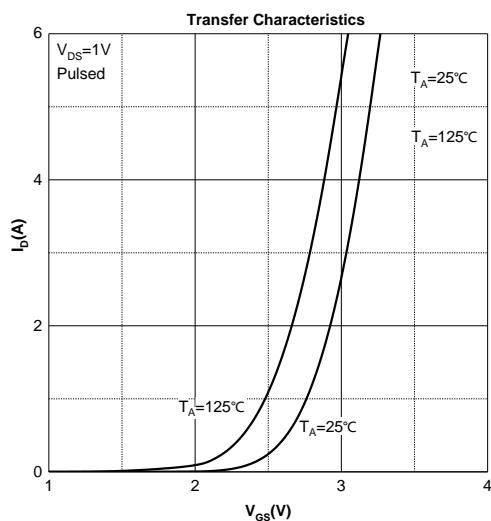
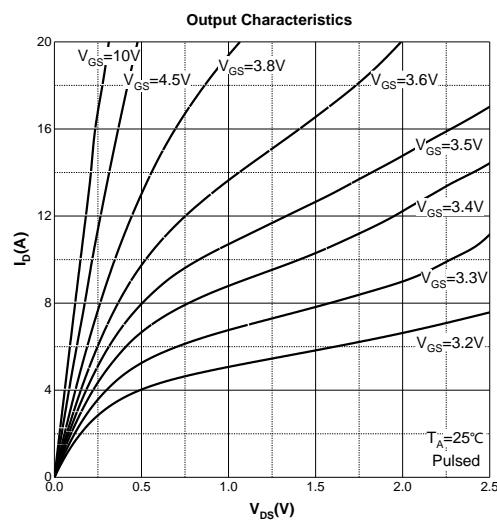
* E_{AS} Test Condition $V_{DD} = 20V$, $V_{GS} = 10V$, $L = 1.0\text{mH}$, $I_{AS} = 11\text{A}$ Starting $T_J = 25^\circ\text{C}$.

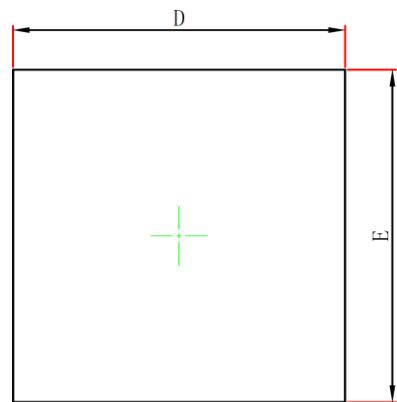
MOSFET ELECTRICAL CHARACTERISTICS($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
On Characteristics						
Gate threshold voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.5	2.5	V
Drainsource onresistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 5\text{A}$		15	20	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 5\text{A}$		20	26	
Forward transconductance	g_{FS}	$V_{\text{DS}} = 10\text{V}, I_D = 5\text{A}$		13		S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		999		pF
Output Capacitance	C_{oss}			95.5		
Reverse Transfer Capacitance	C_{rss}			73.6		
Gate resistance	R_g	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		3.09		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 5\text{A}$		10		nC
GateSource Charge	Q_{gs}			1.8		
GateDrain Charge	Q_{gd}			2.5		
Turnon delay time	$t_{d(\text{on})}$	$V_{\text{DD}} = 15\text{V}, R_G = 3\Omega, V_{\text{GS}} = 10\text{V}, R_L = 3\Omega$		12		ns
Turnon rise time	t_r			3		
Turnoff delay time	$t_{d(\text{off})}$			50		
Turnoff fall time	t_f			5		
Diode Characteristics						
Diode Forward Voltage	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_S = 5\text{A}$			1.2	V

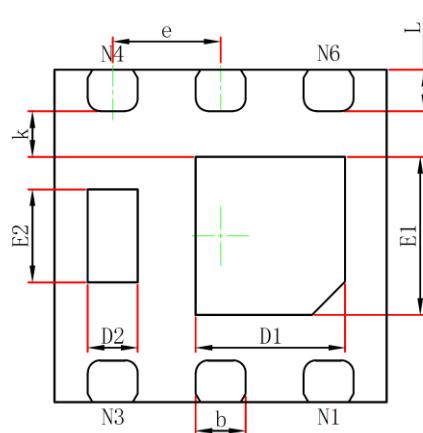
Notes :

1. $R_{\theta\text{JA}}$ is measured with the device mounted on 1 in² FR4 board with 1oz. single side copper, in a still air environment with $T_A = 25^\circ\text{C}$.

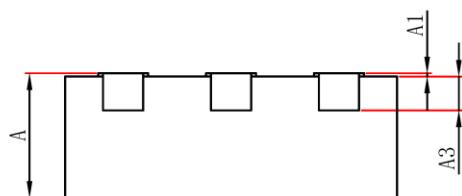
Typical Characteristics


DFN2X2-6L Package Information


TOP VIEW



BOTTOM VIEW



SIDE VIEW

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0	0.050	0	0.002
A3	2.03REF		0.008REF	
D	1.900	2.100	0.075	0.083
E	1.900	2.100	0.075	0.083
D1	0.800	1.000	0.031	0.039
E1	0.850	1.050	0.033	0.041
D2	0.200	0.400	0.008	0.016
E2	0.460	0.660	0.018	0.026
k	0.200MIN		0.008MIN	
b	0.250	0.350	0.010	0.014
e	0.65BSC		0.026TYP	
L	0.174	0.326	0.007	0.013