



### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
40V	1.85m $\Omega$ @10V	130A
	2.35m $\Omega$ @4.5V	

### Feature

- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

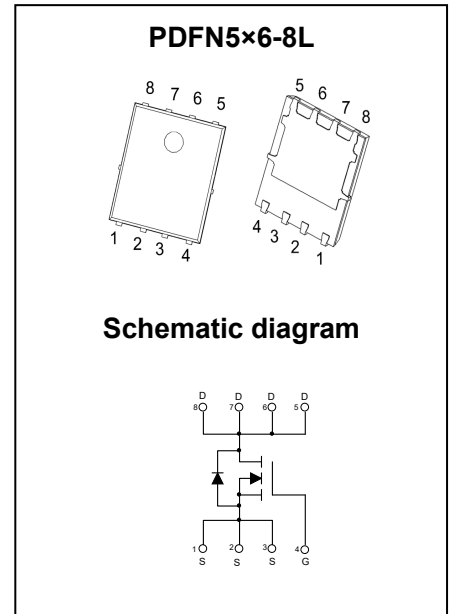
### Application

- Power Switching Application

### MARKING:



M025N04N = Device Code  
XX = Date Code  
Solid Dot = Green Indicator



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain - Source Voltage	$V_{DS}$	40	V
Gate - Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D$	130	A
Continuous Drain Current <sup>6</sup>	$I_D$	44	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	440	A
Single Pulsed Avalanche Current <sup>3</sup>	$I_{AS}$	66	A
Single Pulsed Avalanche Energy <sup>3</sup>	$E_{AS}$	1089	mJ
Power Dissipation <sup>5</sup>	$P_D$	78	W
Power Dissipation <sup>6</sup>	$P_D$	2.5	W
Thermal Resistance from Junction to Ambient <sup>6</sup>	$R_{\theta JA}$	50	$^\circ\text{C}/\text{W}$
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.6	$^\circ\text{C}/\text{W}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55~ +150	$^\circ\text{C}$

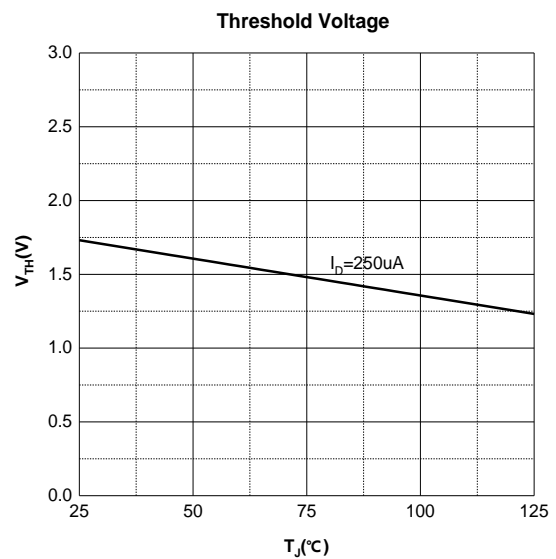
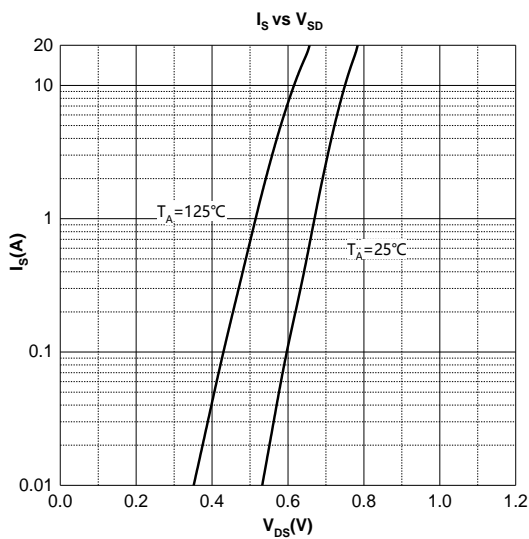
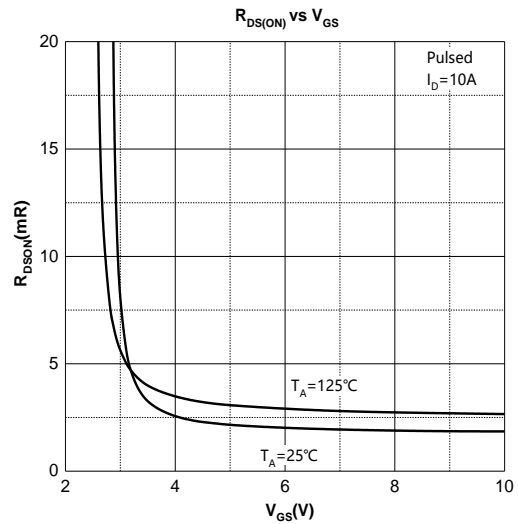
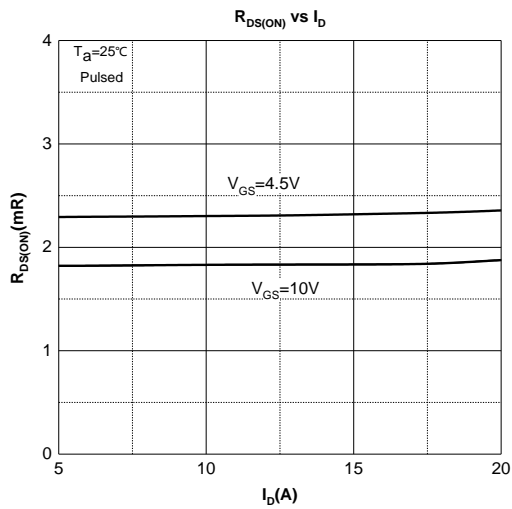
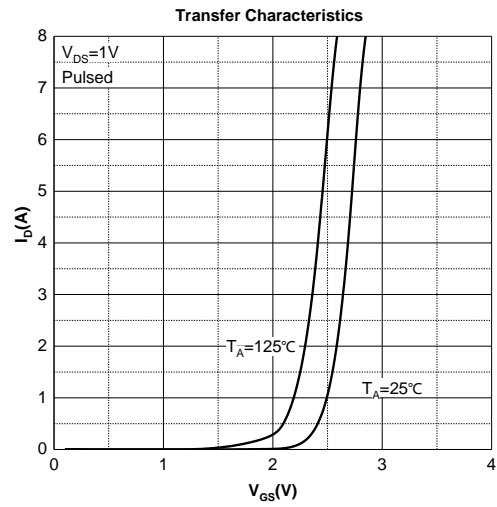
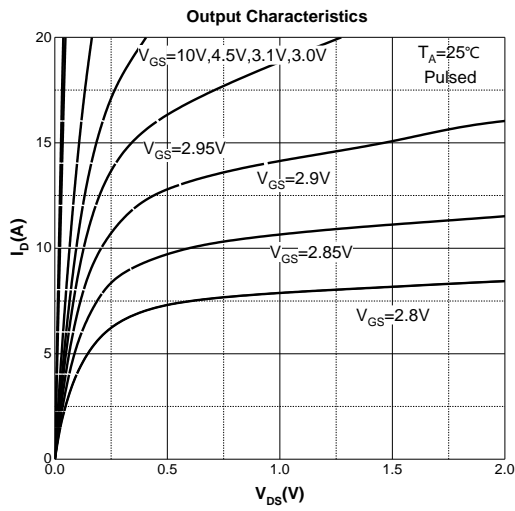
## MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

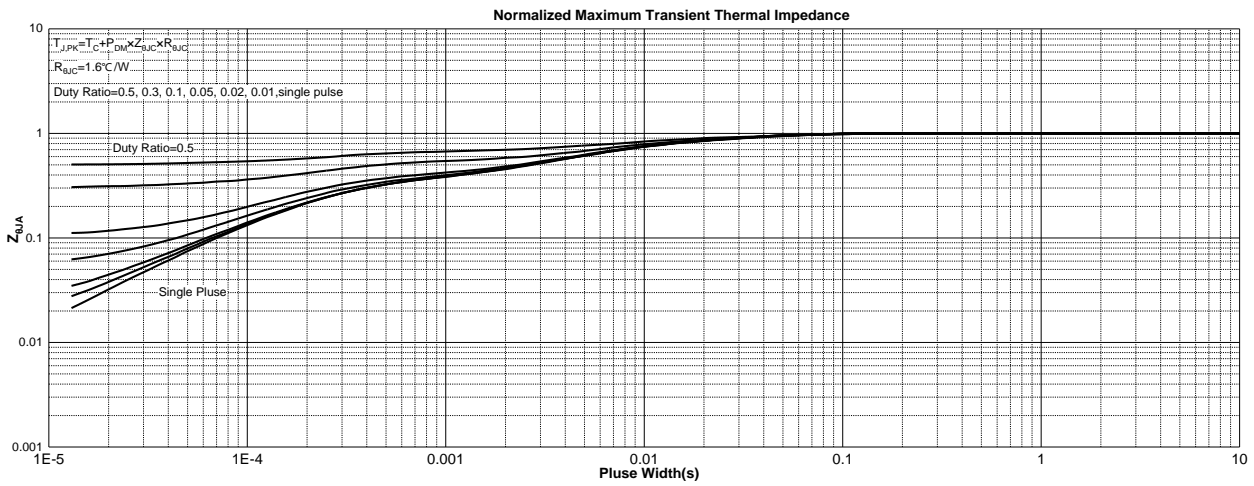
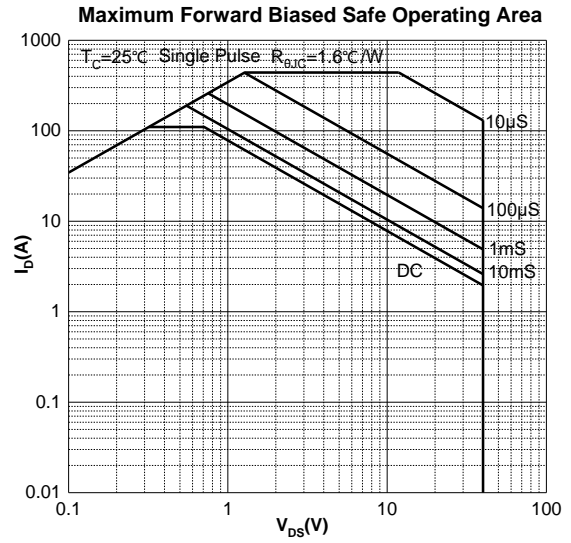
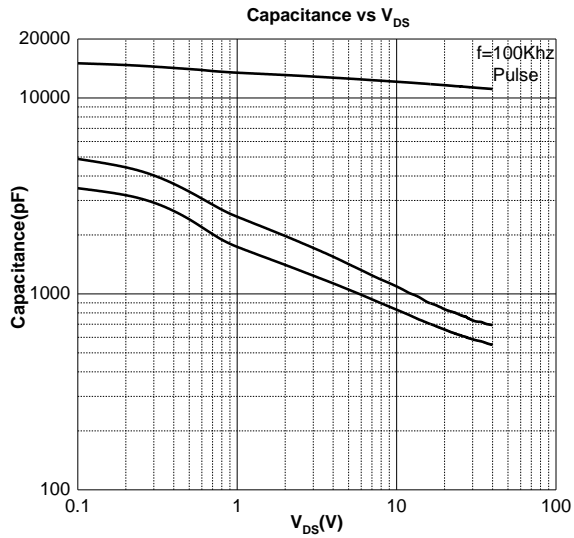
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	40			V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 32V, V_{GS} = 0V$			1	$\mu A$
Gate - Body Leakage Current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$			$\pm 100$	nA
<b>On Characteristics<sup>4</sup></b>						
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.0	1.7	3.0	V
Drain-source On-resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$		1.85	2.5	m $\Omega$
		$V_{GS} = 4.5V, I_D = 20A$		2.35	3.5	
Forward Transconductance	$g_{FS}$	$V_{DS} = 10V, I_D = 10A$	20			S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 20V, V_{GS} = 0V, f = 100KHz$		11700		pF
Output Capacitance	$C_{oss}$			798		
Reverse Transfer Capacitance	$C_{rss}$			645		
Gate Resistance	$R_g$	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$		1.2		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{DS} = 20V, V_{GS} = 10V, I_D = 20A$		150		nC
Gate-source Charge	$Q_{gs}$			34.5		
Gate-drain Charge	$Q_{gd}$			12		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 20V, V_{GS} = 10V, R_L = 1\Omega$ $R_G = 3\Omega$		33		ns
Turn-on Rise Time	$t_r$			10.5		
Turn-off Delay Time	$t_{d(off)}$			108		
Turn-off Fall Time	$t_f$			9		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	$V_{SD}$	$V_{GS} = 0V, I_S = 20A$			1.2	V

Notes :

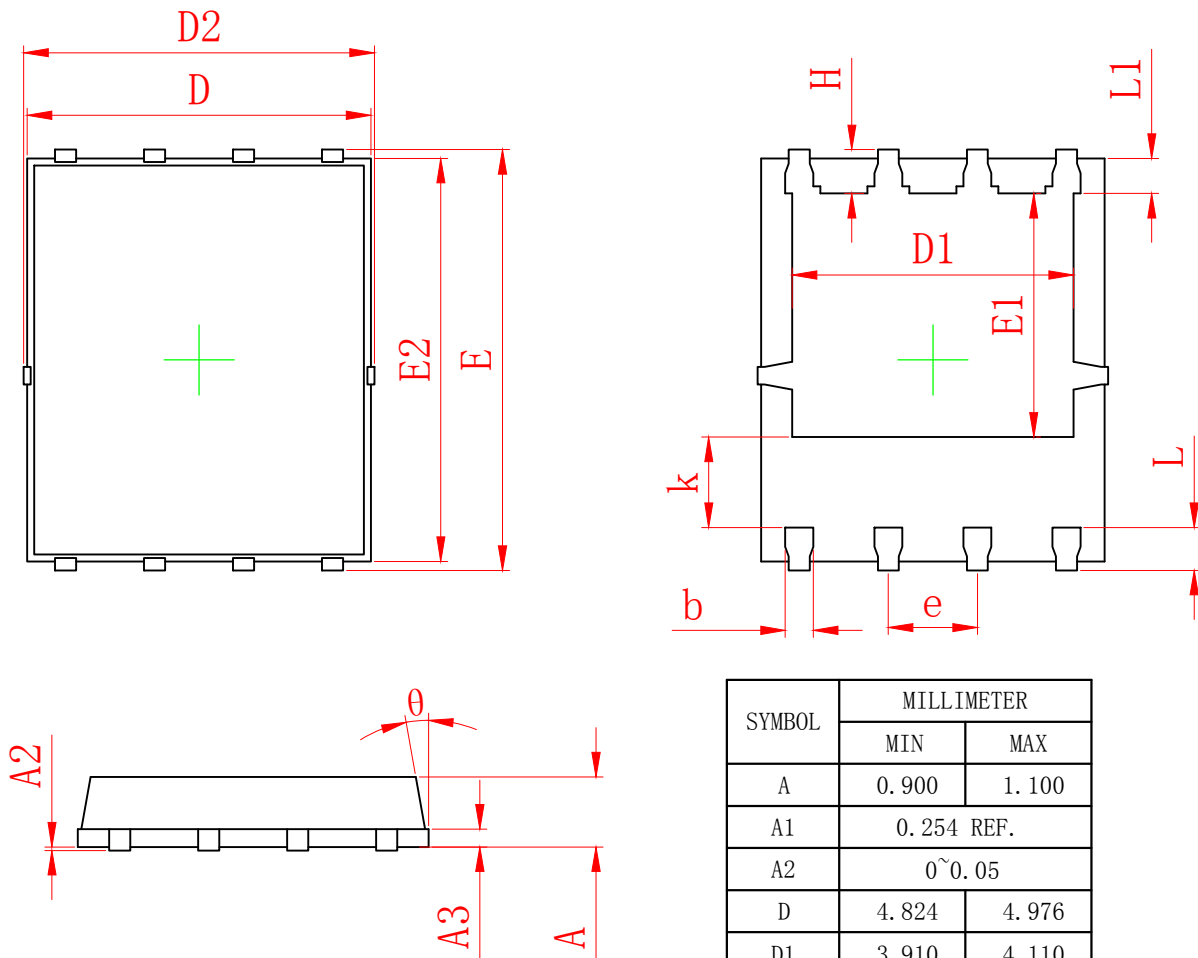
- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width  $\leq 10\mu s$ , duty cycle  $\leq 1\%$ .
- 3.EAS condition:  $V_{DD} = 25V, V_{GS} = 10V, L = 0.5mH, R_G = 25\Omega$  Starting  $T_J = 25^\circ\text{C}$ .
- 4.Pulse Test : Pulse Width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
- 5.The power dissipation  $P_D$  is limited by  $T_{J(MAX)} = 150^\circ\text{C}$ .And device mounted on a large heatsink
- 6.Device mounted on  $1in^2$  FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

**Typical Characteristics**





## PDFN5×6-8L Package Information



SYMBOL	MILLIMETER	
	MIN	MAX
A	0.900	1.100
A1	0.254 REF.	
A2	0~0.05	
D	4.824	4.976
D1	3.910	4.110
D2	4.944	5.076
E	5.924	6.076
E1	3.375	3.575
E2	5.674	5.826
b	0.350	0.450
e	1.270 TYP.	
L	0.534	0.686
L1	0.424	0.576
k	1.190	1.390
H	0.549	0.701
$\theta$	8°	12°