



GP
ELECTRONICS

GPM086P03NMA
30V P-Channel MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
-30V	6.8mΩ@-10V	-40A
	8.5mΩ@-4.5V	

Feature

- Trench Technology Power MOSFET
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Application

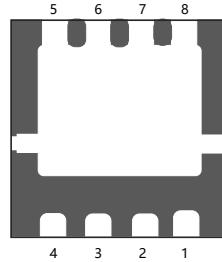
- Power Switching Application

MARKING:

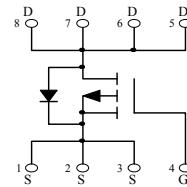


M086P03N = Device Code
XX = Date Code
Solid Dot = Green Indicator

PLP3.3x3.3-8L



Schematic diagram



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

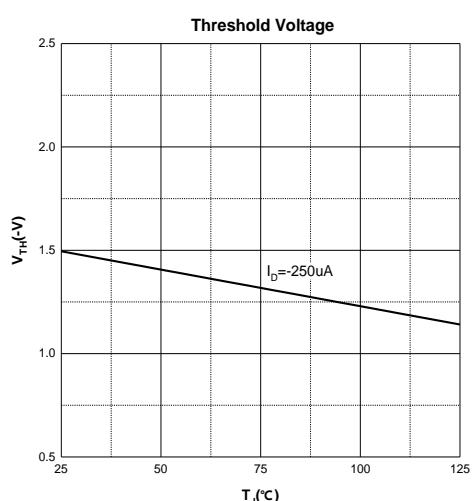
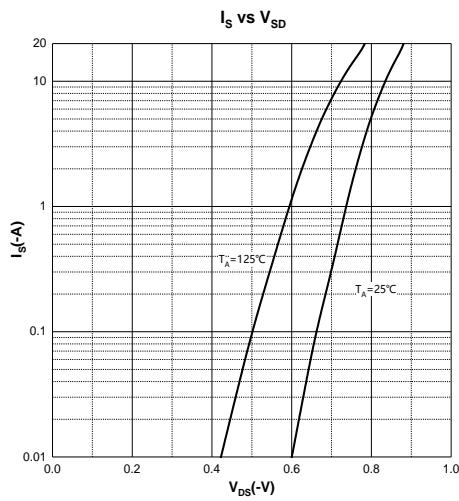
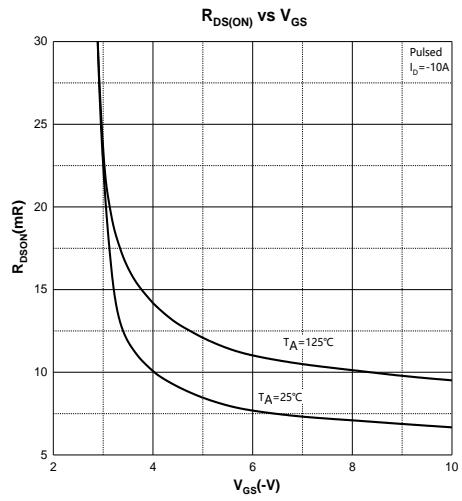
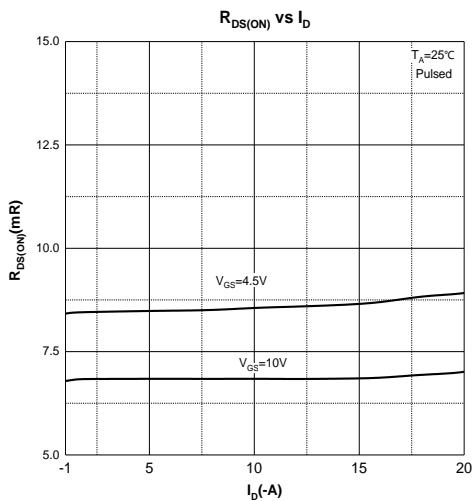
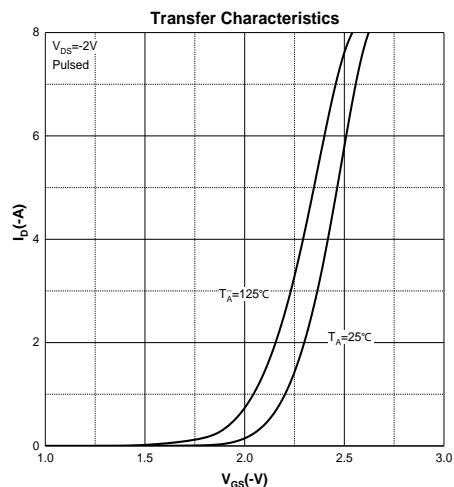
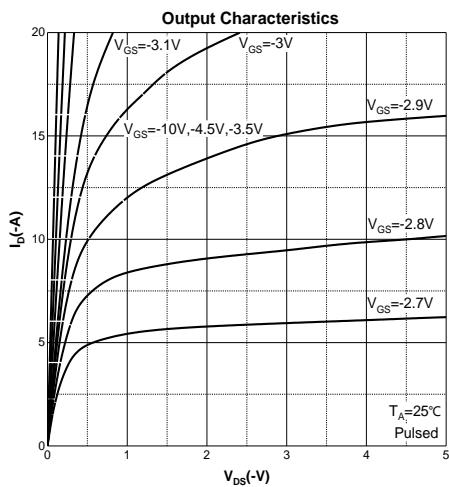
Parameter	Symbol	Value	Unit
Drain - Source Voltage	V_{DS}	-30	V
Gate - Source Voltage	V_{GS}	± 25	V
Continuous Drain Current ¹	I_D	-40	A
Continuous Drain Current ¹	I_D	-28	A
Pulsed Drain Current ²	I_{DM}	-128	A
Single Pulsed Avalanche Current ³	I_{AS}	-28	A
Single Pulsed Avalanche Energy ³	E_{AS}	196	mJ
Power Dissipation ⁵	P_D	96	W
Thermal Resistance from Junction to Ambient ⁶	$R_{\theta JA}$	75	°C/W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.3	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

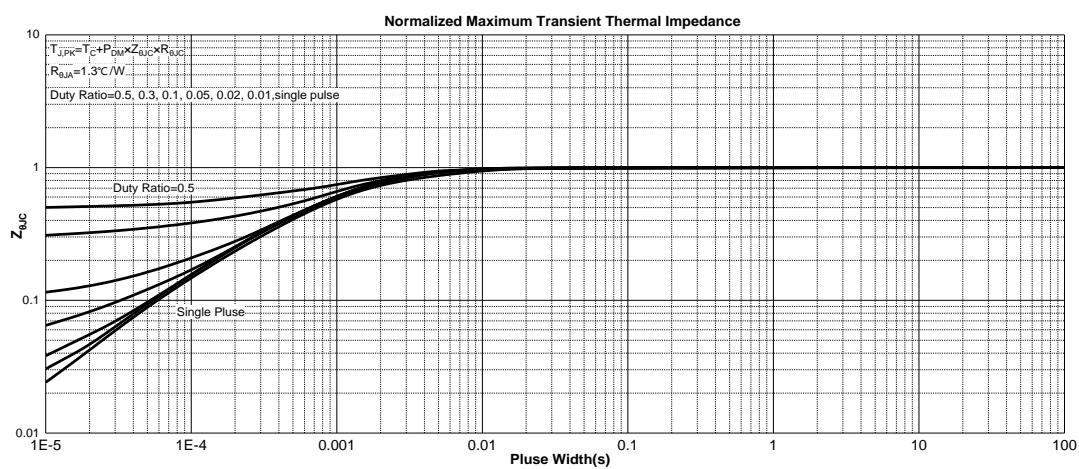
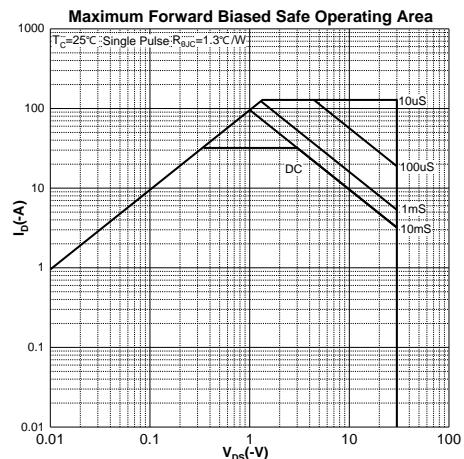
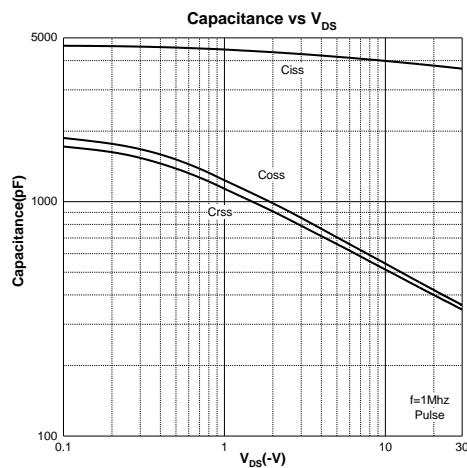
MOSFET ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = -24\text{V}, V_{\text{GS}} = 0\text{V}$			-1	μA
Gate - Body Leakage Current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
On Characteristics⁴						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1.0	-1.5	-3.0	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -5.0\text{A}$		6.8	8.6	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -5.0\text{A}$		8.5	13	
Forward Transconductance	g_{FS}	$V_{\text{DS}} = -10\text{V}, I_D = -5.0\text{A}$	10			S
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		3840		pF
Output Capacitance	C_{oss}			464		
Reverse Transfer Capacitance	C_{rss}			441		
Gate Resistance	R_g	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		5		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = -10\text{V}, I_D = -10\text{A}$		60		nC
Gate-source Charge	Q_{gs}			15		
Gate-drain Charge	Q_{gd}			18		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -15\text{V}, V_{\text{GS}} = -10\text{V}, R_L = 3\Omega, R_G = 3\Omega$		25		ns
Turn-on Rise Time	t_r			11		
Turn-off Delay Ttime	$t_{\text{d}(\text{off})}$			136		
Turn-off Fall Time	t_f			49		
Source - Drain Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$V_{\text{GS}} = 0\text{V}, I_s = -5.0\text{A}$			-1.2	V

Notes :

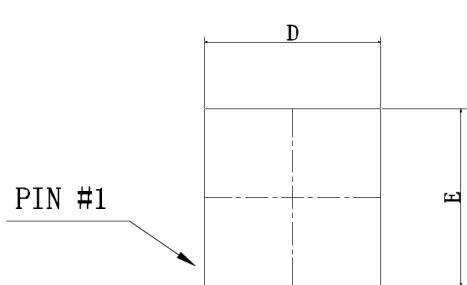
- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width $\leq 10\mu\text{s}$, duty cycle $\leq 1\%$.
- 3.E_{AS} condition: $V_{\text{DD}} = -15\text{V}, V_{\text{GS}} = -10\text{V}, L = 0.5\text{mH}, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
- 4.Pulse Test : Pulse Width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 5.The power dissipation P_D is limited by $T_{J(\text{MAX})} = 150^\circ\text{C}$.And device mounted on a large heatsink
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

Typical Characteristics


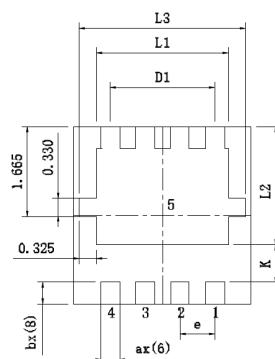


PLP3.3x3.3-8L Package Information

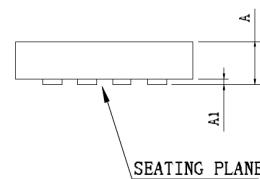
symbol	Dimension in mm		
	MIN	NOM	MAX
A	0.650	0.700	0.750
A1	0.050	0.100	0.150
D	3.200	3.300	3.400
E	3.200	3.300	3.400
D1	---	1.950	---
e	---	0.650	---
ax(6)	0.300	0.350	0.400
bx(8)	0.350	0.400	0.450
L1	2.400	2.450	2.500
L2	2.150	2.200	2.250
L3	3.050	3.100	3.150
K	0.600	0.700	0.800



Top View



Bottom View



Side View