

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
85V	4.5mΩ@10V	120A

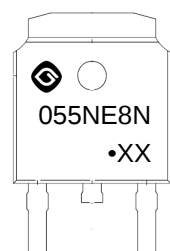
Feature

- Split Gate Trench Technology
- Low $R_{DS(on)}$
- Low Gate Charge
- Low Gate Resistance
- 100% UIS Tested

Application

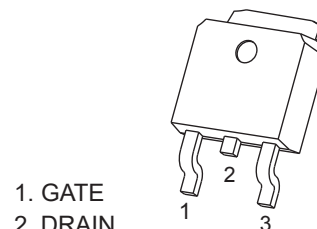
- Power Switching Application

MARKING:



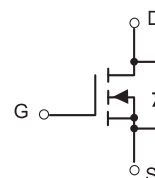
055NE8N = Device Code
 XX = Date Code
 Solid Dot = Green Indicator

TO-252-2L



1. GATE
2. DRAIN
3. SOURCE

Schematic diagram



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter		Symbol	Value	Unit
Drain - Source Voltage		V_{DS}	85	V
Gate - Source Voltage		V_{GS}	±20	V
Continuous Drain Current ¹	$T_C = 25^\circ\text{C}$	I_D	120	A
Pulsed Drain Current ²		I_{DM}	480	A
Single Pulsed Avalanche Current ³		I_{AS}	48.7	A
Single Pulsed Avalanche Energy ³		E_{AS}	541.1	mJ
Power Dissipation ⁵	$T_C = 25^\circ\text{C}$	P_D	138.9	W
Thermal Resistance from Junction to Ambient ⁶		$R_{\theta JA}$	61	$^\circ\text{C/W}$
Thermal Resistance from Junction to Case		$R_{\theta JC}$	0.9	$^\circ\text{C/W}$
Junction Temperature		T_J	150	$^\circ\text{C}$
Storage Temperature		T_{STG}	-55~ +150	$^\circ\text{C}$

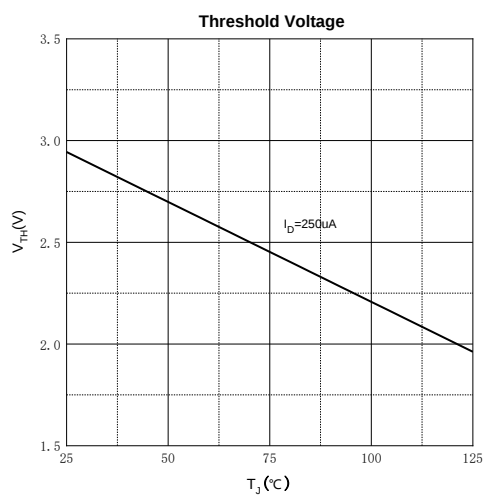
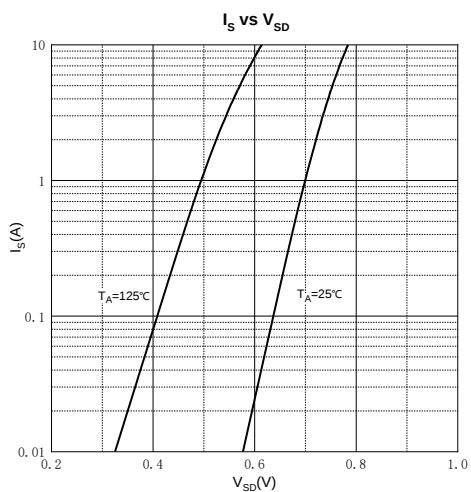
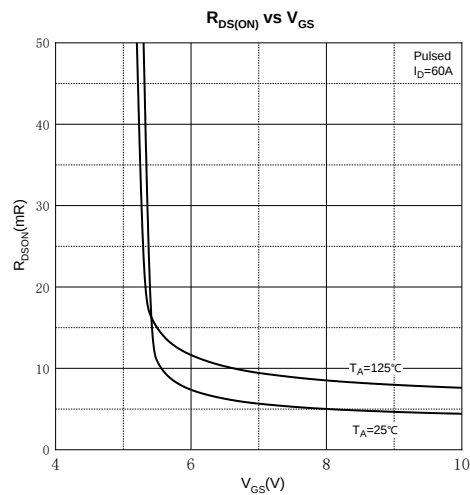
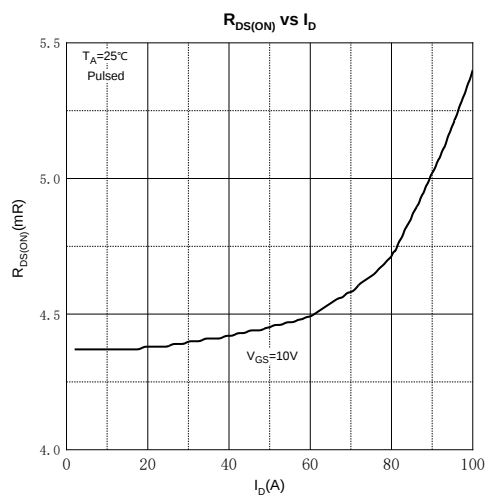
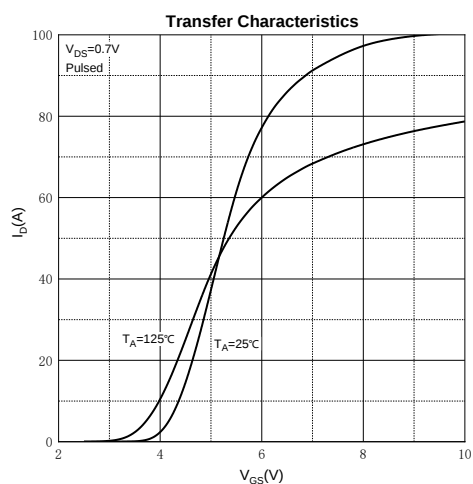
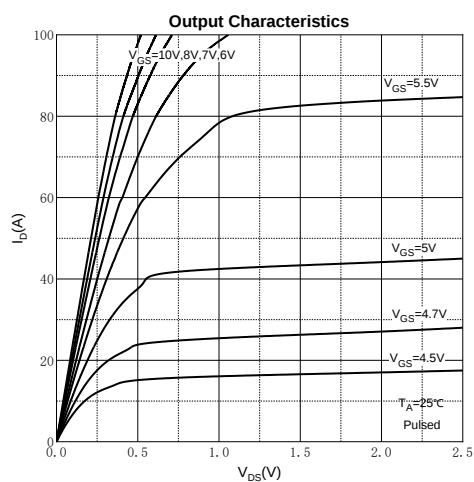
MOSFET ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

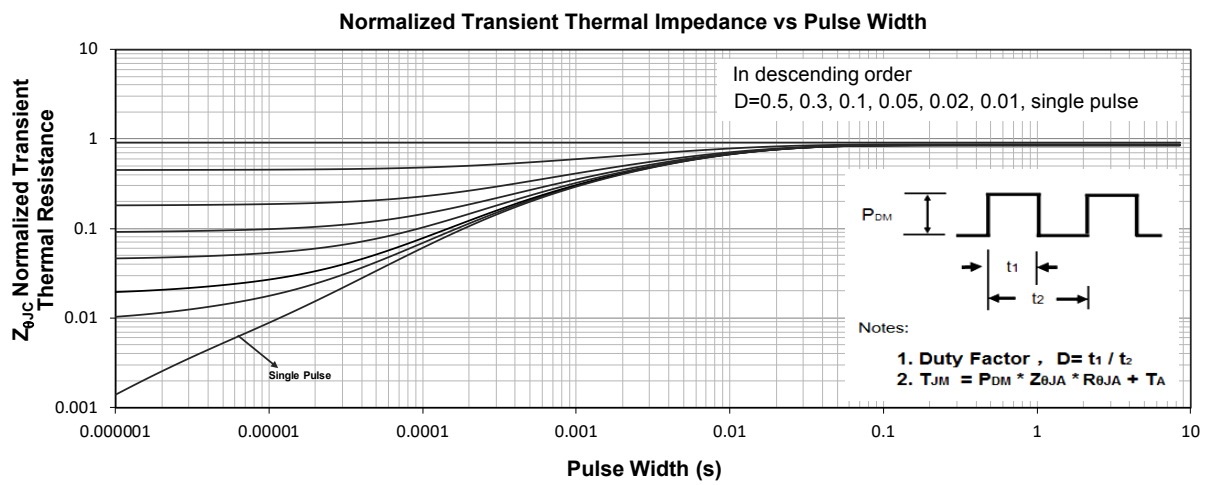
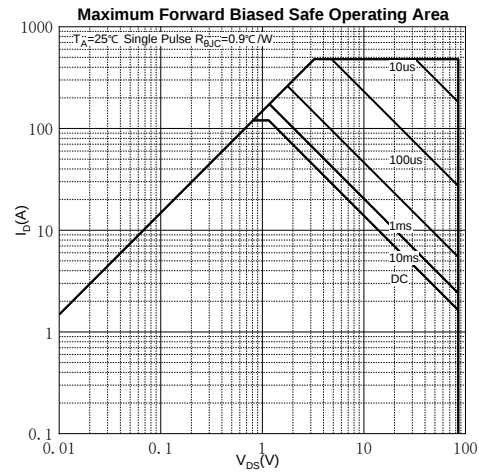
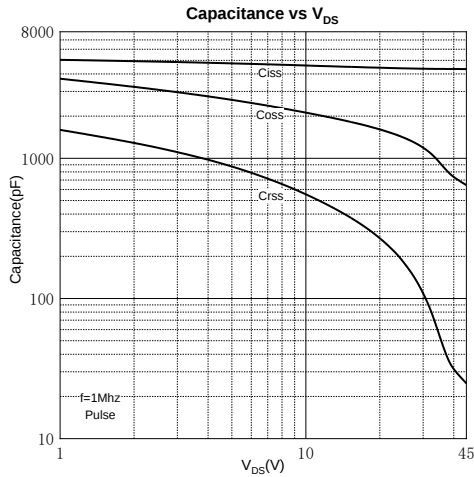
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	85			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 85V, V _{GS} = 0V			1	μA
Gate - Body Leakage Current	I _{GSS}	V _{GS} = ±20V, V _{DS} = 0V			±100	nA
On Characteristics ⁴						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	2.0	2.9	4.0	V
Drain-source On-resistance	R _{DS(on)}	V _{GS} = 10V, I _D =50A		4.5	5.4	mΩ
Forward Transconductance	g _{FS}	V _{DS} = 5V, I _D = 20A		57.8		S
Dynamic Characteristics						
Input Capacitance	C _{iss}	V _{DS} = 42.5V, V _{GS} = 0V, f = 1MHz		4317		pF
Output Capacitance	C _{oss}			682.3		
Reverse Transfer Capacitance	C _{rss}			29.1		
Gate Resistance	R _g	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz		1.7		Ω
Switching Characteristics						
Total Gate Charge	Q _g	V _{DS} = 40V, V _{GS} = 10V, I _D = 50A		76.8		nC
Gate-source Charge	Q _{gs}			15.1		
Gate-drain Charge	Q _{gd}			4.8		
Turn-on Delay Time	t _{d(on)}	V _{DD} =40V, V _{GS} = 10V, R _L = 2.5Ω R _G = 3Ω		16.5		ns
Turn-on Rise Time	t _r			51.8		
Turn-off Delay Ttime	t _{d(off)}			37.1		
Turn-off Fall Time	t _f			8.2		
Source - Drain Diode Characteristics						
Diode Forward Voltage ⁴	V _{SD}	V _{GS} = 0V, I _S = 50A			1.4	V

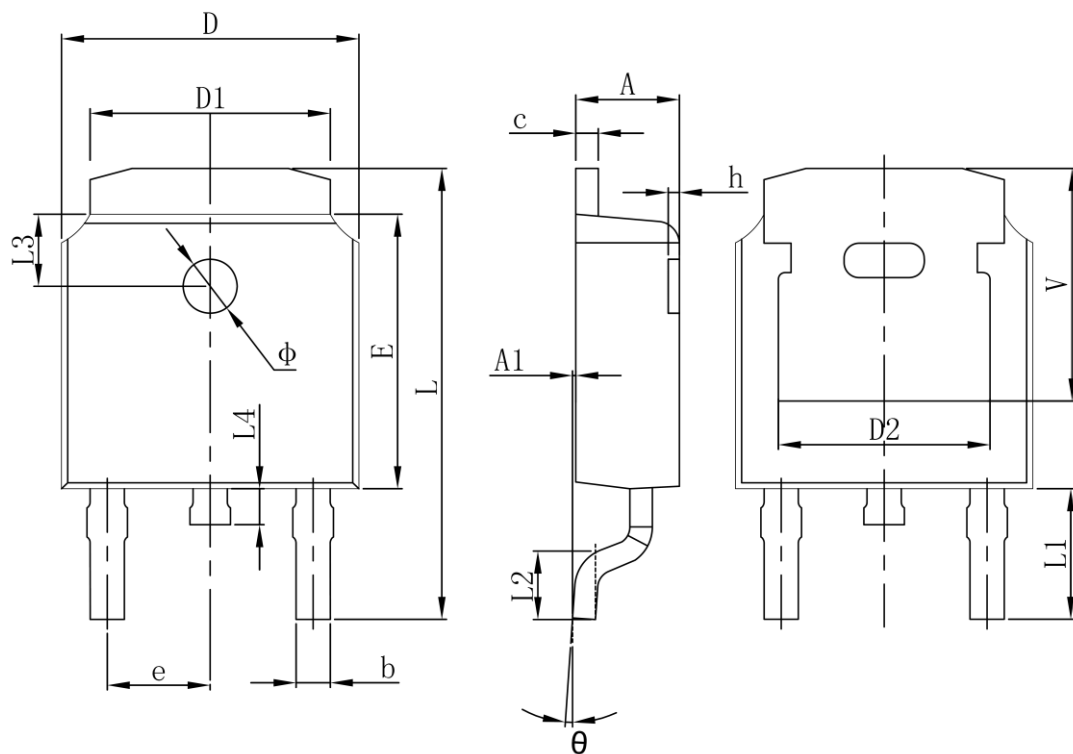
Notes :

- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width $\leq 10\mu s$, duty cycle $\leq 1\%$.
- 3.EAS condition: $V_{DD} = 48V, V_{GS} = 10V, L = 0.5mH, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
- 4.Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
- 5.The power dissipation P_D is limited by $T_{J(MAX)} = 150^\circ\text{C}$.And device mounted on a large heatsink
- 6.Device mounted on $1in^2$ FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

Typical Characteristics





TO-252-2L Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.400	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.635	0.770	0.025	0.030
c	0.460	0.580	0.018	0.023
D	6.500	6.700	0.256	0.264
D1	5.100	5.460	0.201	0.215
D2	4.830REF		0.190REF	
E	6.000	6.200	0.236	0.244
e	2.186	2.386	0.086	0.094
L	9.712	10.312	0.382	0.406
L1	2.900REF		0.114REF	
L2	1.400	1.700	0.055	0.067
L3	1.600REF		0.063REF	
L4	0.600	1.000	0.024	0.039
Φ	1.100	1.300	0.043	0.051
θ	0°	8°	0°	8°
h	0.000	0.300	0.000	0.012
V	5.250REF		0.207REF	