

### Product Summary

The GPL6221 series are a group of positive voltage regulators manufactured by CMOS technologies with high ripple rejection, ultra-low noise, low power consumption and low dropout voltage, which can prolong battery life in portable electronics. The GPL6221 series work with low-ESR ceramic capacitors, reducing the amount of board space necessary for power applications. The GPL6221 series consume less than  $0.1\mu A$  in shutdown mode and have fast turn-on time less than  $50\mu S$ . The series are very suitable for the battery-powered equipment, such as RF applications and other systems requiring a quiet voltage source.

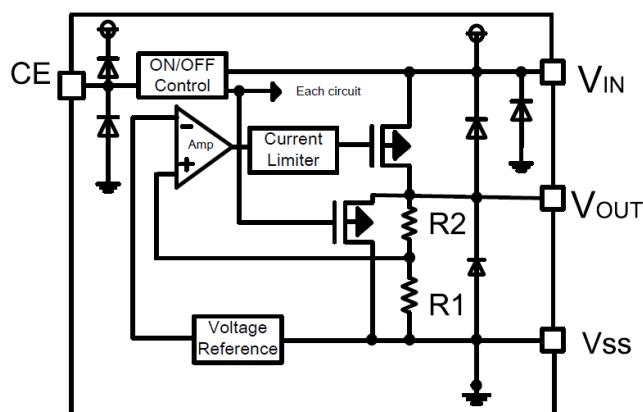
### Features

- Low Quiescent Current:  $25\mu A$
- High Ripple Rejection:  $85dB@1kHz$
- Operating Voltage Range:  $1.8V \sim 6.0V$
- Low Dropout Voltage:  $50mV@100mA$
- Output Voltage:  $0.8V \sim 5.0V$
- High Accuracy:  $\pm 2\%$ (Typ.)
- TTL-Logic-Controlled Shutdown Input
- Excellent Line and Load Transient Response
- Built-in Current Limiter, Short-Circuit Protection

### Applications

- Cellular and Smart Phones
- Radio control systems
- Laptop, Palmtops and PDAs
- Digital Still and Video Cameras
- MP3, MP4 Player
- Battery-Powered Equipment

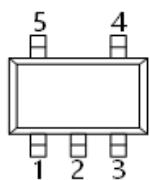
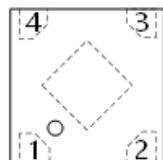
### Block Diagram



### Order Information

GPL6221V①②

Designator	Description
①	Output Voltage e.g. 1.8V=18
②	Package: SOT-23-5L=K5 DFN1*1-4=H1

**Pin Configuration**
**SOT-23-5L**

**DFN1\*1-4L**


Pin Number	Pin Name	Function
<b>SOT-23-5L</b>		
1	V <sub>IN</sub>	Power Input Pin
2	V <sub>SS</sub>	Ground
3	CE	Chip Enable Pin
4	NC	No Connection
5	V <sub>OUT</sub>	Output Pin

**WBFBP-04C/DFN1\*1-4L**

Pin Number	Pin Name	Function
1	V <sub>OUT</sub>	Output Pin
2	V <sub>SS</sub>	Ground
3	CE	Chip Enable Pin
4	V <sub>IN</sub>	Power Input Pin

**Absolute Maximum Ratings (T<sub>a</sub>=25°C,unless otherwise noted)**

Parameter	Symbol	Ratings	Units
Input Voltage <sup>(2)</sup>	V <sub>IN</sub>	V <sub>SS</sub> -0.3~V <sub>SS</sub> +7	V
Output Voltage <sup>(2)</sup>	V <sub>OUT</sub>	V <sub>SS</sub> -0.3~ V <sub>IN</sub> +0.3	V
Output Current	I <sub>OUT</sub>	500	mA
Power Dissipation	SOT-23-5L	P <sub>D</sub>	0.38
	DFN1*1-4	P <sub>D</sub>	0.40
Thermal Resistance	SOT-23-5L	θ <sub>JA</sub>	258
	DFN1*1-4	θ <sub>JA</sub>	250
Operating Free Air Temperature Range	T <sub>A</sub>	-40~85	°C
Operating Junction Temperature Range	T <sub>j</sub>	-40~125	°C
Storage Temperature	T <sub>stg</sub>	-40~125	°C
Lead Temperature(Soldering, 10 sec)	T <sub>solder</sub>	260	°C
ESD rating <sup>(4)</sup>	Human Body Model -(HBM)	2	kV
	Machine Model- (MM)	200	V

(1) Stresses beyond those listed under *absolute maximum ratings* may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *recommended operating conditions* is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

(2)All voltages are with respect to network ground terminal.

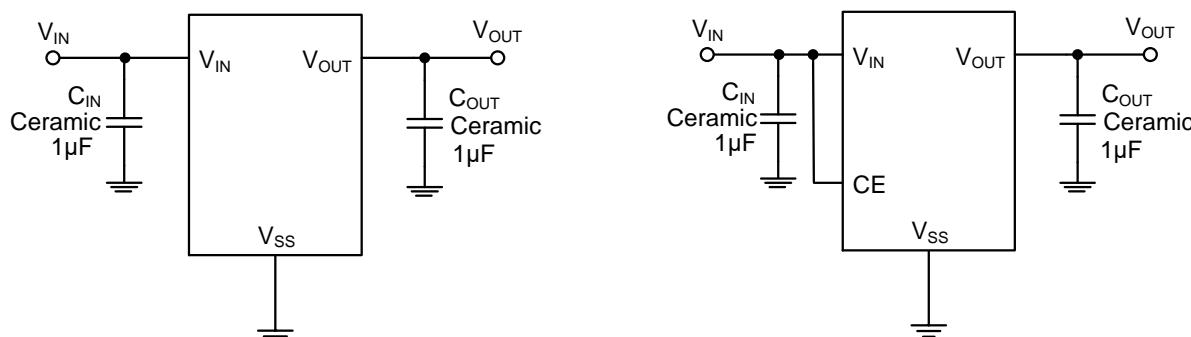
**Electrical Characteristics( $V_{IN}=V_{OUT}+1V$ ,  $C_{IN}=C_{OUT}=1\mu F$ ,  $T_A=25^\circ C$ , unless otherwise specified)**

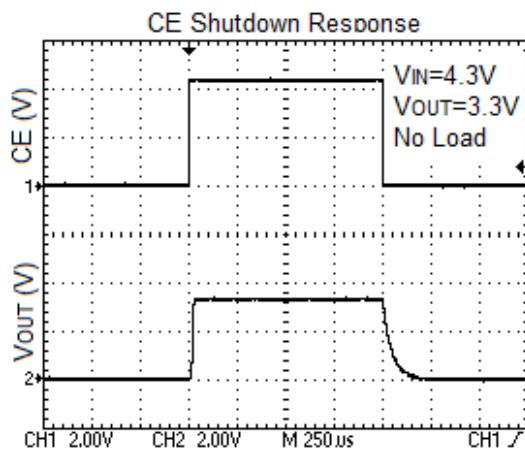
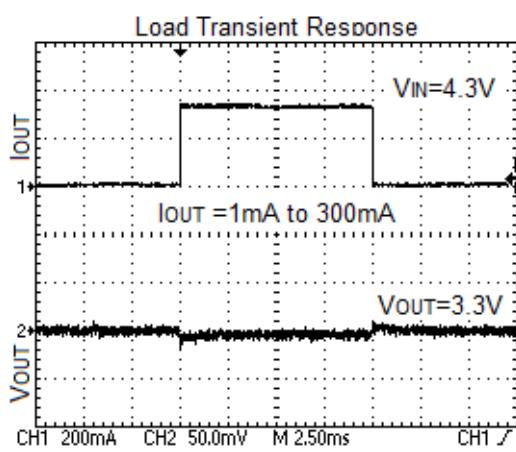
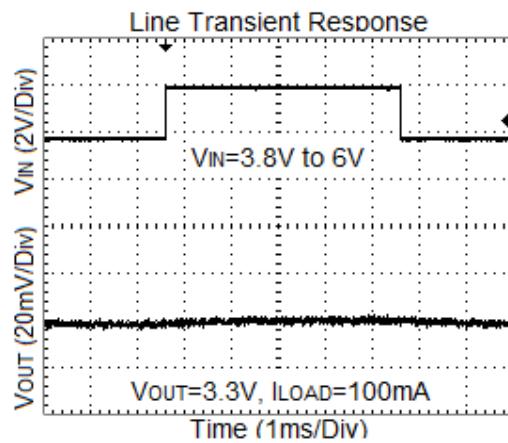
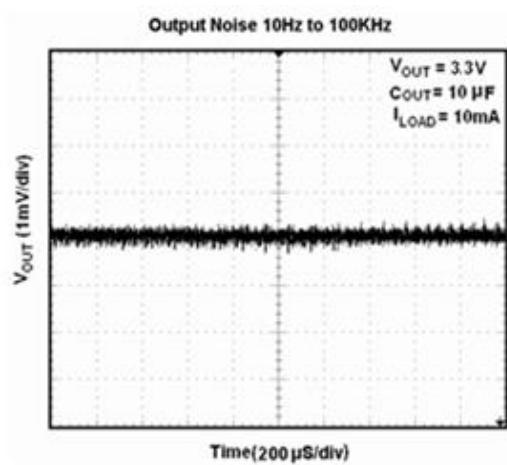
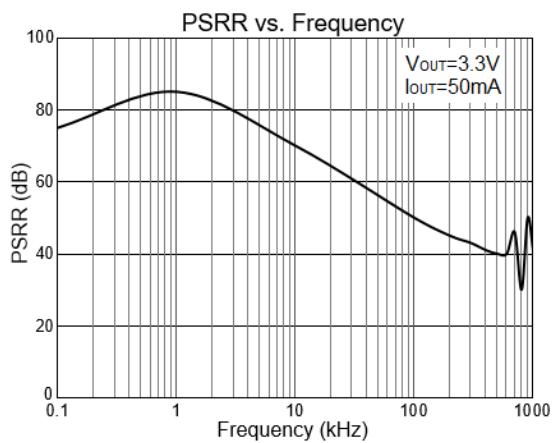
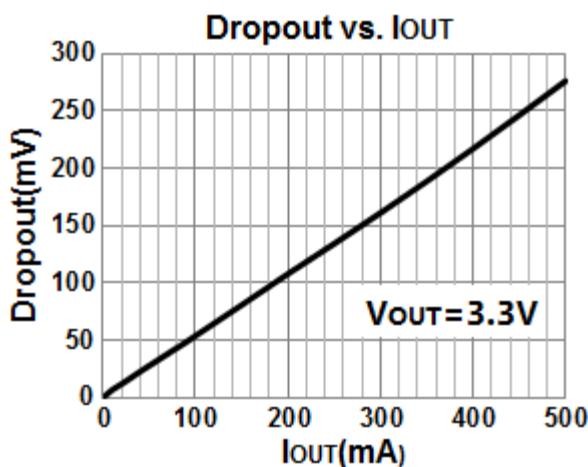
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Output Voltage	$V_{OUT(E)}$ <sup>2)</sup>	$I_{OUT}=1mA$	$V_{OUT}*0.98$	$V_{OUT}$	$V_{OUT}*1.02$	V
Supply Current	$I_{SS}$	$I_{OUT}=0$		25	50	$\mu A$
Standby Current	$I_{STBY}$	$CE = V_{SS}$		0.1	1	$\mu A$
Output Current	$I_{OUT}$	$V_{OUT}\geq 1.0V$	500	700		mA
		$V_{OUT}\geq 0.8V$	300	500		
Dropout Voltage <sup>3)</sup>	$V_{dif}$	$I_{OUT} = 100mA$ $V_{OUT}\geq 3.3V$		50		mV
Load Regulation	$\Delta V_{OUT}$	$V_{IN} = V_{OUT} + 1V$ , $1mA \leq I_{OUT} \leq 100mA$		1		mV
Line Regulation		$I_{OUT} = 10mA$ $V_{OUT} + 1V \leq V_{IN} \leq 6V$		0.01	0.2	%/V
Output Voltage Temperature Characteristics		$I_{OUT} = 10mA$ $-40 \leq T \leq +85$		50		ppm
Short Current	$I_{Short}$	$V_{OUT} = V_{SS}$		50		mA
Input Voltage	$V_{IN}$	—	1.8		6.0	V
Power Supply Rejection Rate	100Hz	$I_{OUT}=50mA$		75		dB
	1kHz			85		
	10kHz			70		
CE "High" Voltage	$V_{CE^{(H)}}$		1.5		$V_{IN}$	V
CE "Low" Voltage	$V_{CE^{(L)}}$				0.3	V
$C_{OUT}$ Auto-Discharge Resistance	$R_{DISCHRG}$	$V_{IN}=5V, V_{OUT}=3.0V$ , $V_{CE}=V_{SS}$		60		$\Omega$

1)  $V_{OUT}$ : Specified Output Voltage.

2)  $V_{OUT(E)}$  : Effective Output Voltage ( ie. The output voltage when  $V_{IN} = (V_{OUT} + 1.0V)$  and maintain a certain  $I_{OUT}$  Value).

3)  $V_{dif}$  : The Difference Of Output Voltage And Input Voltage When Input Voltage Is Decreased Gradually Till Output Voltage Equals To 98% Of  $V_{OUT}$  (E).

**Typical Application**


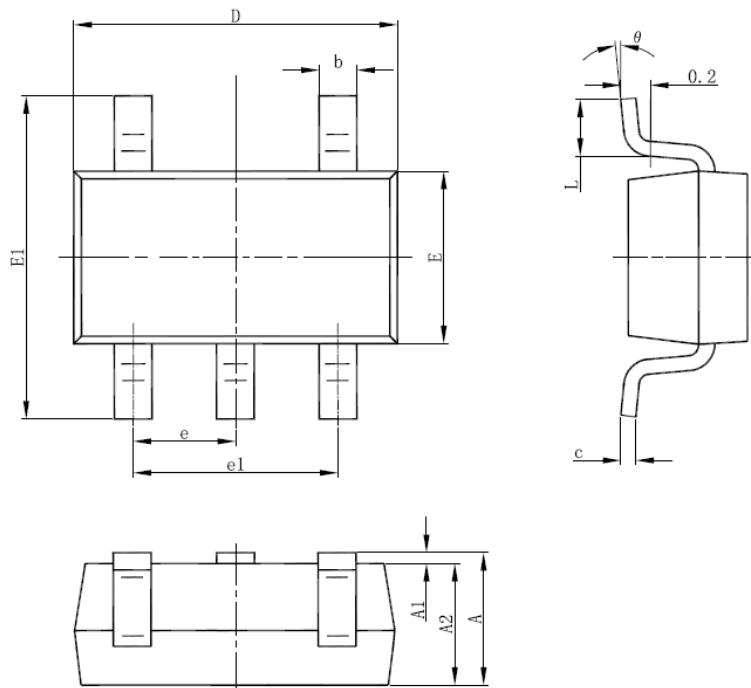
**Typical Performance Characteristics**


### C<sub>OUT</sub> Auto-Discharge Function

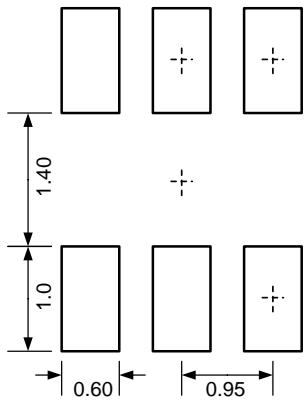
GPL6221 series can discharge the electric charge in the output capacitor (C<sub>OUT</sub>), when a low signal to the CE pin, which enables a whole IC circuit turn off, is inputted via the N-channel transistor located between the V<sub>OUT</sub> pin and the V<sub>SS</sub> pin (cf. BLOCK DIAGRAM). The C<sub>OUT</sub> auto-discharge resistance value is set at 60Ω (V<sub>OUT</sub>=3.0V @ V<sub>IN</sub>=5.0V at typical). The discharge time of the output capacitor (C<sub>OUT</sub>) is set by the C<sub>OUT</sub> auto-discharge resistance (R) and the output capacitor (C<sub>OUT</sub>). By setting time constant of a C<sub>OUT</sub> auto-discharge resistance value [R<sub>DISCHRG</sub>] and an output capacitor value (C<sub>OUT</sub>) as  $\tau$  ( $\tau=C \times R_{DISCHRG}$ ), the output voltage after discharge via the N-channel transistor is calculated by the following formulas.

$$V = V_{OUT(E)} \times e^{-t/\tau}, \text{ or } t = \tau \ln(V/V_{OUT(E)})$$

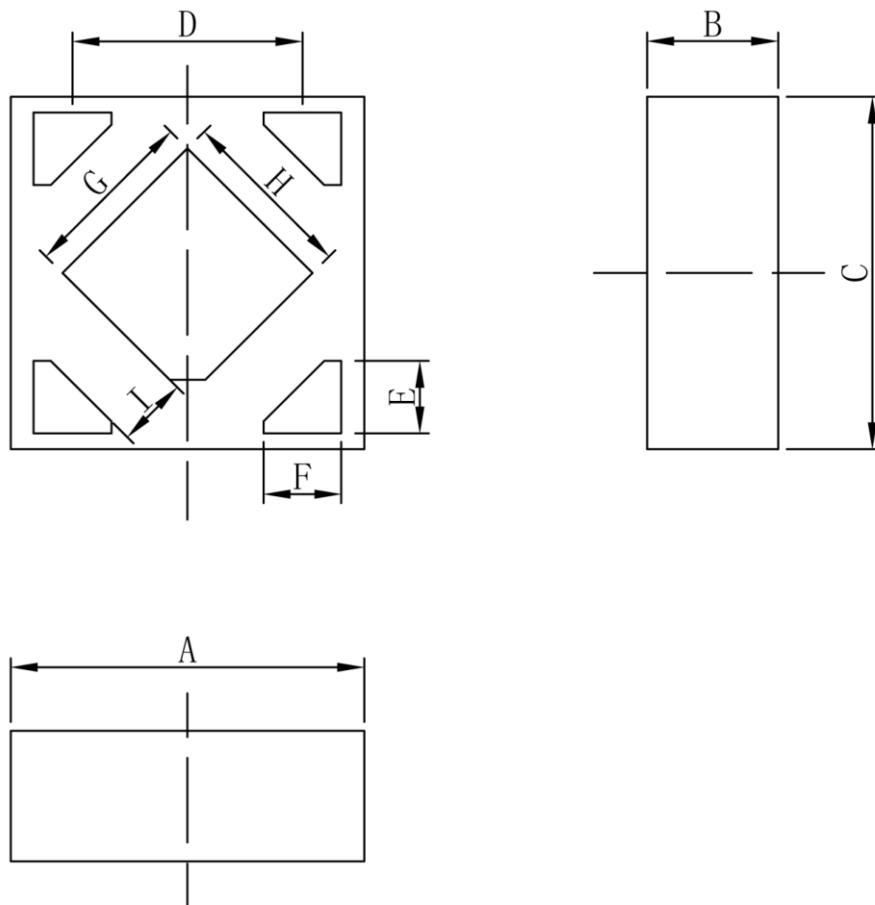
(V: Output voltage after discharge, V<sub>OUT (E)</sub>: Output voltage, t: Discharge time.  $\tau$ : C<sub>OUT</sub> auto-discharge resistance R<sub>DISCHRG</sub> × Output capacitor (C<sub>OUT</sub>) value C)

**SOT-23-5L Package Outline Dimensions**


Symbol	Dimensions In Millimeters	
	Min.	Max.
A	1.050	1.250
A1	0.000	0.100
A2	1.050	1.150
b	0.300	0.500
c	0.100	0.200
D	2.820	3.020
E	1.500	1.700
E1	2.650	2.950
e	0.950(BSC)	
e1	1.800	2.000
L	0.300	0.600
θ	0°	8°

**SOT-23-5L Suggested Pad Layout (Unit: mm)**

**Notes:**

1. General tolerance:  $\pm 0.05\text{mm}$ .
2. The pad layout is for reference purposes only.

**DFN1\*1-4L Package Outline Dimensions**


Symbol	Dimensions in millimeters	
	Min.	Max.
A	0.950	1.050
B	0.320	0.420
C	0.950	1.050
D	0.600	0.700
E	0.175	0.275
F	0.170	0.270
G	0.440	0.540
H	0.440	0.540
I	0.140	0.240