



**GP**  
**ELECTRONICS**

**GPM300N04LSC**  
**40V N-Channel MOSFET**

### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
40V	29mΩ@10V	5A
	42mΩ@4.5V	

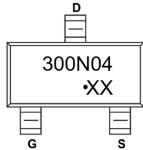
### Feature

- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge

### Application

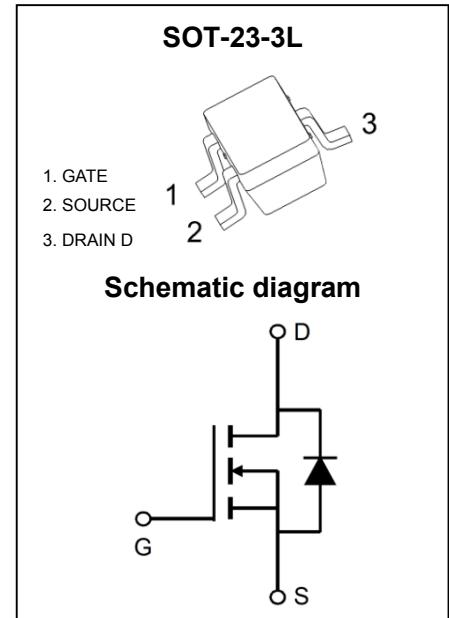
- Load Switch
- DC/DC Converter

### MARKING:



300N04 = Device Code

XX = Date Code



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain - Source Voltage	$V_{DS}$	40	V
Gate - Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1,5</sup>	$I_D$	5	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	20	A
Power Dissipation <sup>4,5</sup>	$P_D$	1.4	W
Thermal Resistance from Junction to Ambient <sup>5</sup>	$R_{\theta JA}$	85	°C/W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55~+150	°C

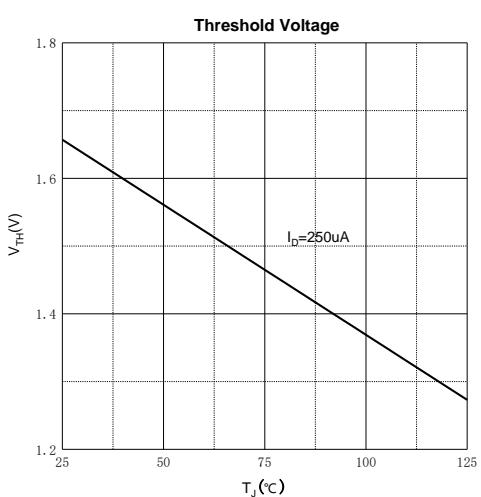
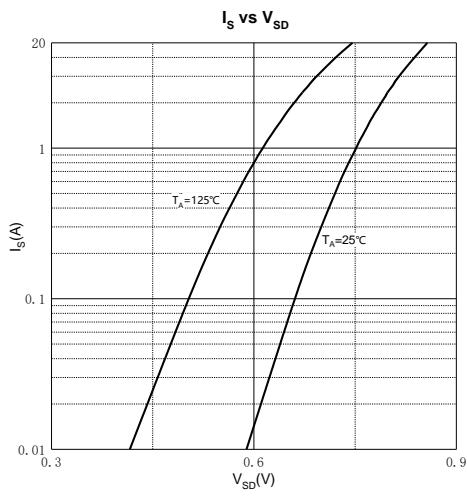
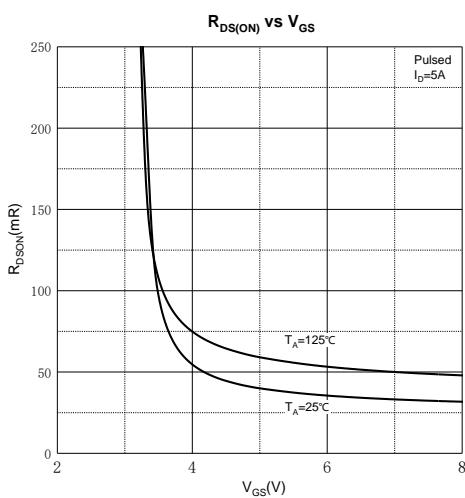
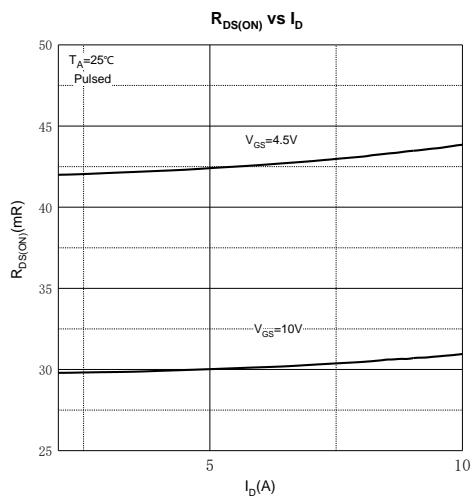
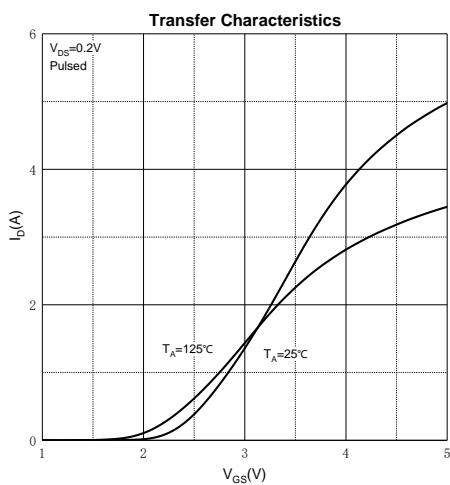
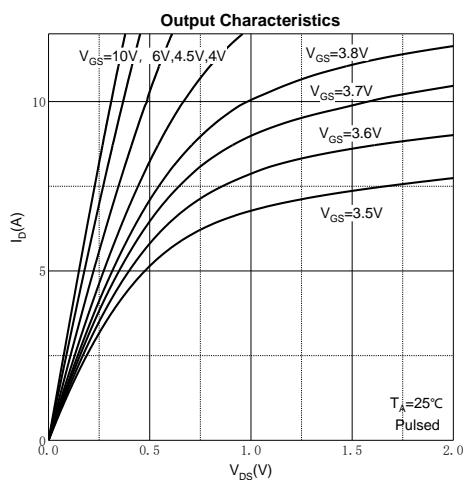
**MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$  unless otherwise noted)**

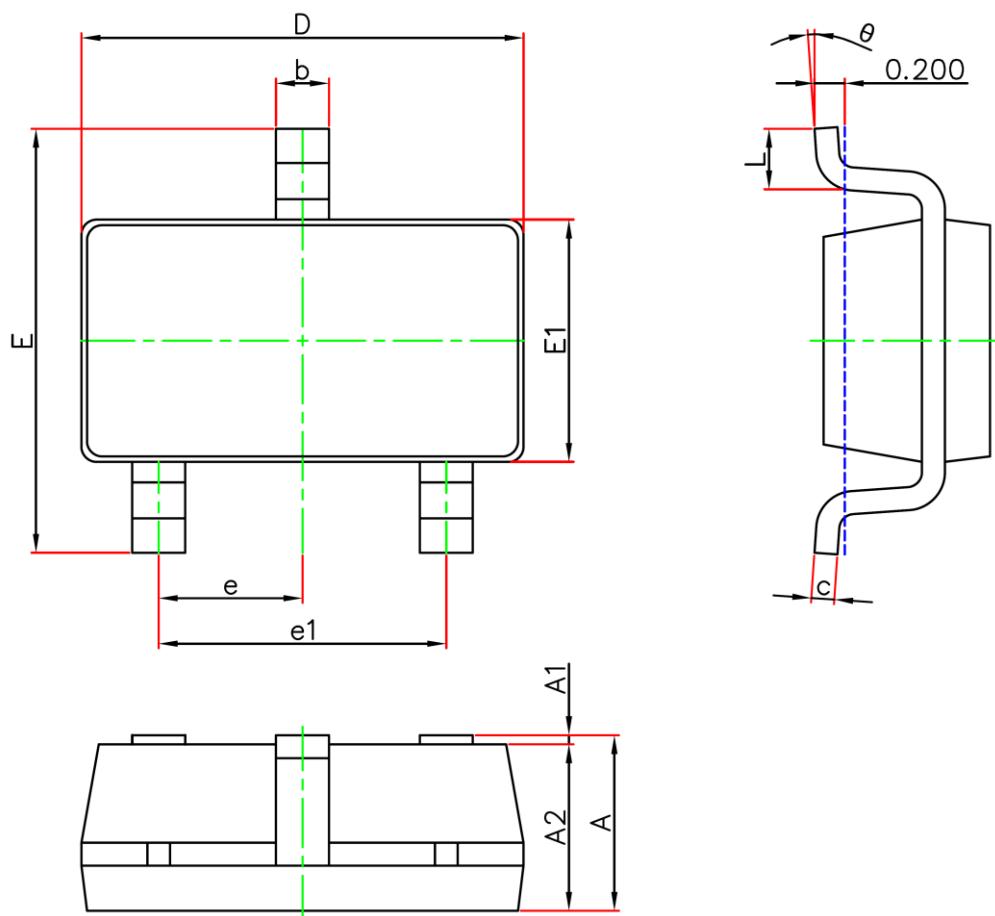
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	40			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 40\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate - Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1	1.65	2.5	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 5\text{A}$		29	35	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 5\text{A}$		42	50	
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{DS}} = 5\text{V}, I_D = 5\text{A}$		22		S
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		366		pF
Output Capacitance	$C_{\text{oss}}$			48		
Reverse Transfer Capacitance	$C_{\text{rss}}$			41		
Gate Resistance	$R_g$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		1.8		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 10\text{V}, I_D = 5\text{A}$		9.3		nC
Gate-source Charge	$Q_{gs}$			1.9		
Gate-drain Charge	$Q_{gd}$			2.0		
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{\text{DD}} = 15\text{V}, V_{\text{GS}} = 10\text{V}, R_L = 2.6\Omega, R_G = 3\Omega$		4.5		ns
Turn-on Rise Time	$t_r$			2.4		
Turn-off Delay Time	$t_{d(\text{off})}$			14.8		
Turn-off Fall Time	$t_f$			2.5		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = 1\text{A}$			1.2	V

Notes :

- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width  $\leq 10\mu\text{s}$ , duty cycle  $\leq 1\%$ .
- 3.Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- 4.The power dissipation  $P_D$  is limited by  $T_{J(\text{MAX})} = 150^\circ\text{C}$ .
- 5.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

## Typical Characteristics



**SOT-23-3L Package Information**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0	0.150	0.000	0.006
A2	1.050	1.250	0.041	0.049
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	2.650	2.950	0.104	0.116
E1	1.500	1.700	0.059	0.067
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°