



**GP**  
**ELECTRONICS**

**GP4407SQ**

**30V P-Channel MOSFET**

### Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
-30V	8.4mΩ@-10V	-12A
	12.5mΩ@-4.5V	

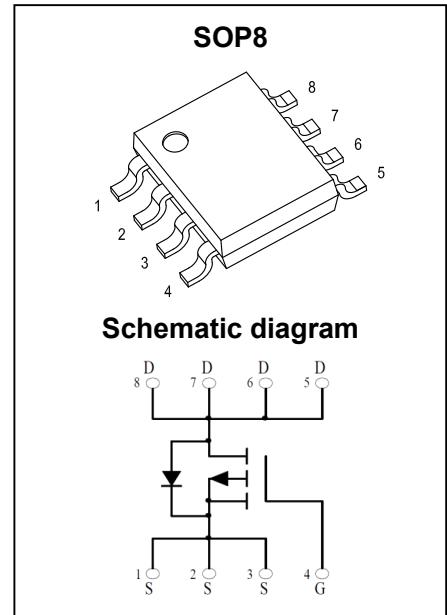
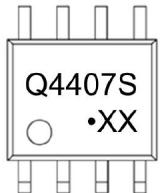
### Feature

- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge

### Application

- Load Switch
- DC/DC Converter

### MARKING:



### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

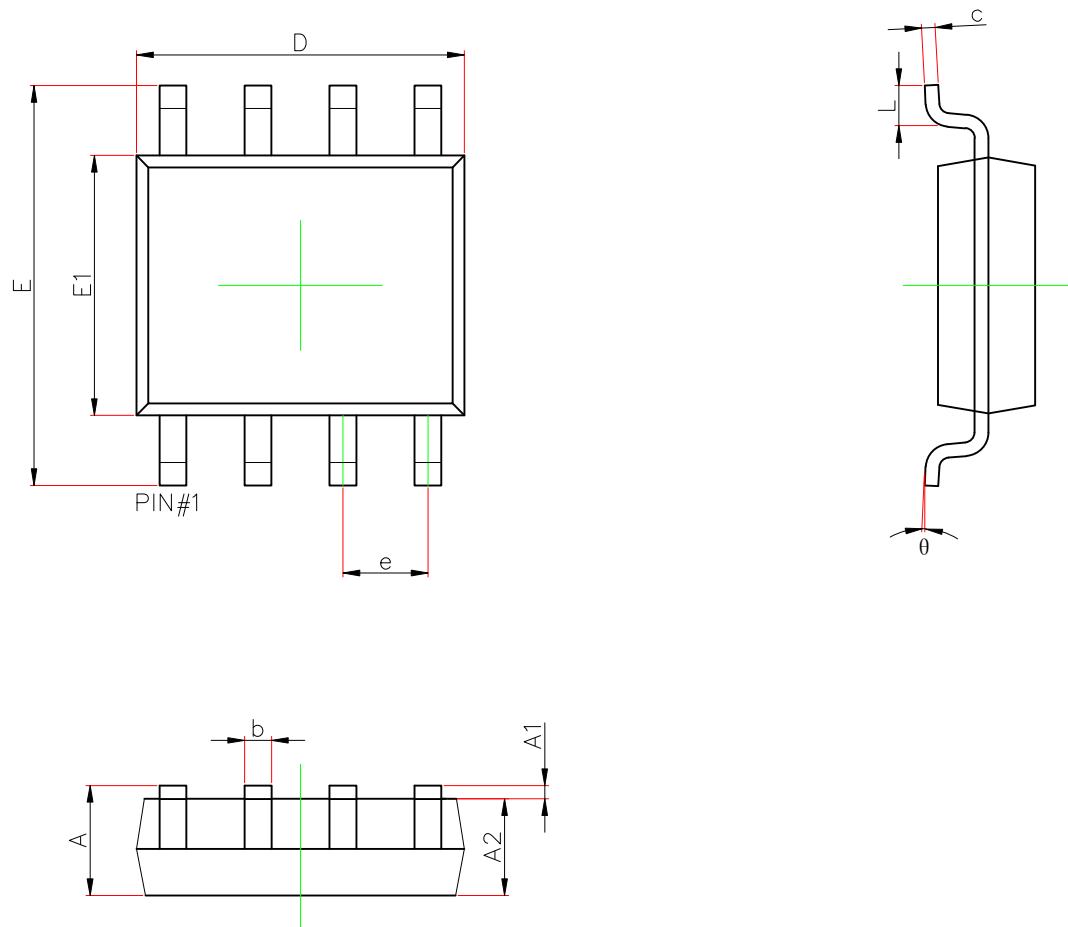
Parameter	Symbol	Value	Unit
Drain - Source Voltage	$V_{DS}$	-30	V
Gate - Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1,6</sup>	$I_D$	-12	A
	$I_D$	-8	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-48	A
Single Pulsed Avalanche Current <sup>3</sup>	$I_{AS}$	-27.5	A
Single Pulsed Avalanche Energy <sup>3</sup>	$E_{AS}$	189	mJ
Power Dissipation <sup>5,6</sup>	$P_D$	1.7	W
Thermal Resistance from Junction to Ambient <sup>6</sup>	$R_{\theta JA}$	75	°C/W
Junction Temperature	$T_J$	150	°C
Storage Temperature	$T_{STG}$	-55~+150	°C

**MOSFET ELECTRICAL CHARACTERISTICS ( $T_J = 25^\circ\text{C}$  unless otherwise noted)**

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-30			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -30\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate - Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-1	-1.7	-3	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -12\text{A}$		8.4	13	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -10\text{A}$		12.5	21	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		2766		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			386		
Reverse Transfer Capacitance	$C_{\text{rss}}$			335		
Gate Resistance	$R_g$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		6.2		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = -15\text{V}, V_{\text{GS}} = -10\text{V}, I_D = -12\text{A}$		51		$\text{nC}$
Gate-source Charge	$Q_{gs}$			6.9		
Gate-drain Charge	$Q_{gd}$			12		
Turn-on Delay Time	$t_{d(\text{on})}$	$V_{\text{DD}} = -15\text{V}, V_{\text{GS}} = -10\text{V}, R_G = 10\Omega$ $R_L = 1.25\Omega$		20		$\text{ns}$
Turn-on Rise Time	$t_r$			14		
Turn-off Delay Time	$t_{d(\text{off})}$			57		
Turn-off Fall Time	$t_f$			20		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>4</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = -2\text{A}$			-1.2	V

Notes :

- 1.The maximum current rating is limited by package.
- 2.Pulse Test : Pulse Width  $\leq 10\mu\text{s}$ , duty cycle  $\leq 1\%$ .
- 3.EAS condition:  $V_{\text{DD}} = -30\text{V}, V_{\text{GS}} = -10\text{V}, L = 0.5\text{mH}, R_G = 25\Omega$  Starting  $T_J = 25^\circ\text{C}$ .
- 4.Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- 5.The power dissipation  $P_D$  is limited by  $T_{J(\text{MAX})} = 150^\circ\text{C}$ .
- 6.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

**SOP8 Package Information**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.156	0.250	0.006	0.010
D	4.700	5.100	0.185	0.201
e	1.270(BSC)		0.050(BSC)	
E	5.800	6.200	0.228	0.244
E1	3.700	4.100	0.146	0.161
L	0.400	1.270	0.016	0.05
θ	0°	8°	0°	8°