



GP
ELECTRONICS

GP4406D33

30V N-Channel MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
30V	8mΩ@10V	20A
	12mΩ@4.5V	

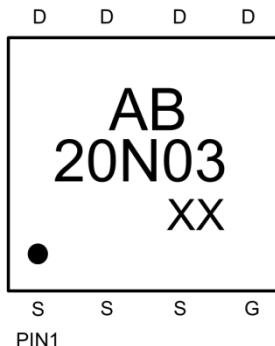
Feature

- High cell density trenched N-ch MOSFETs
- Super low gate charge
- Advanced high cell density Trench technology

Application

- Battery protection applications
- Load switch

MARKING:

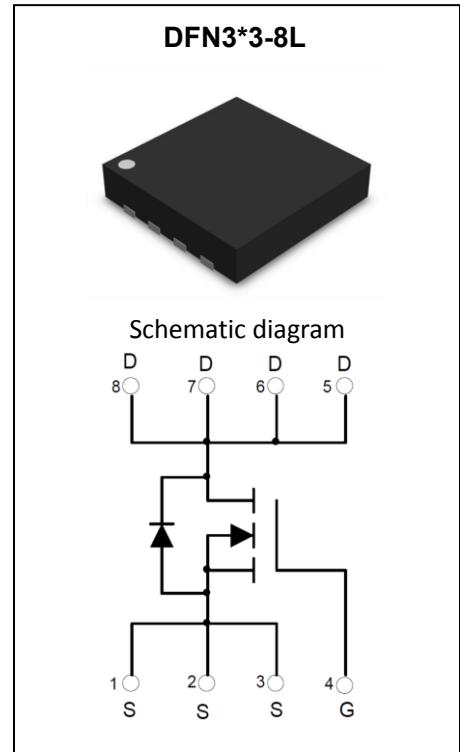


AB20N03= Device code

Solid dot=Pin1 indicator

XX=Date Code

PIN1



ABSOLUTE MAXIMUM RATINGS ($T_c=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D^{(1)}$	20	A
Pulsed Drain Current	$I_{DM}^{(1), (2)}$	45	A
Power Dissipation	$P_D^{(3)}$	3	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	42	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

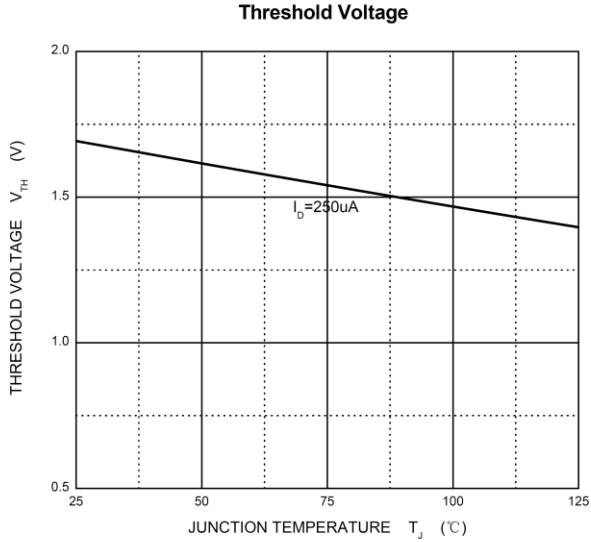
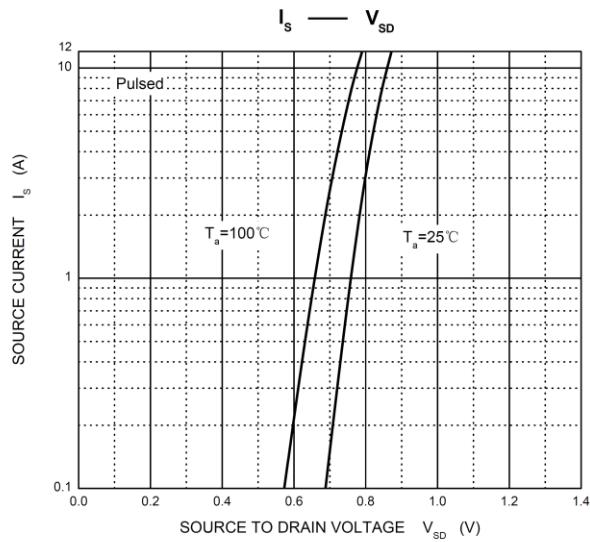
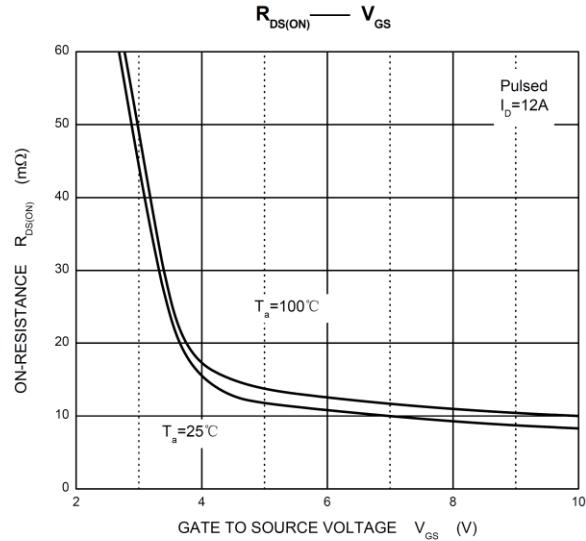
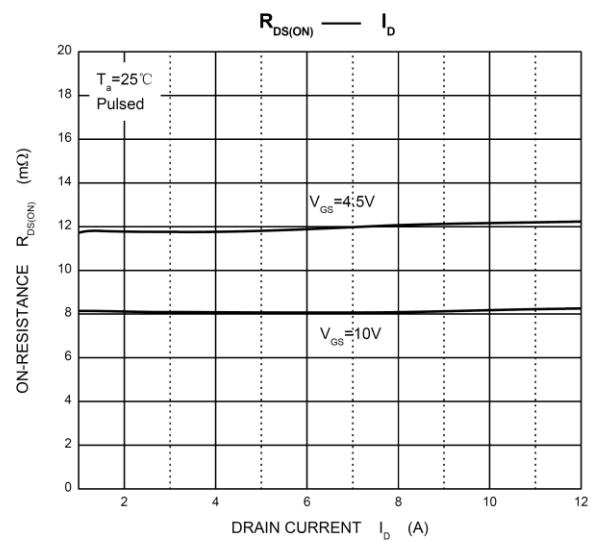
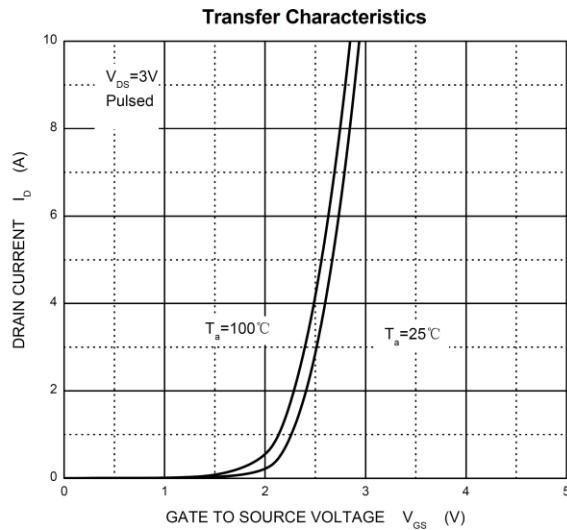
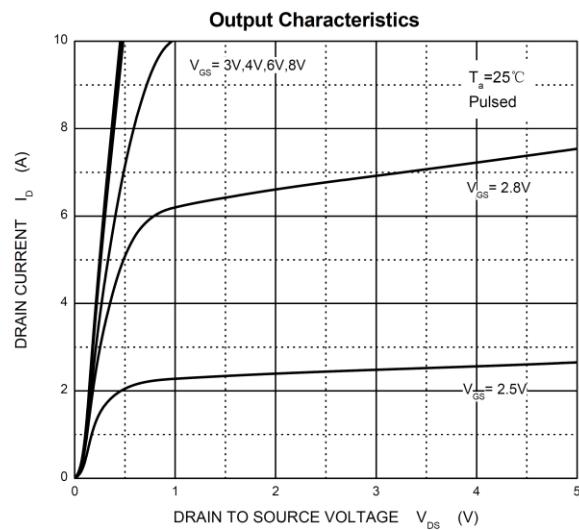
MOSFET ELECTRICAL CHARACTERISTICS ($T_J=25^\circ\text{C}$ unless otherwise noted)

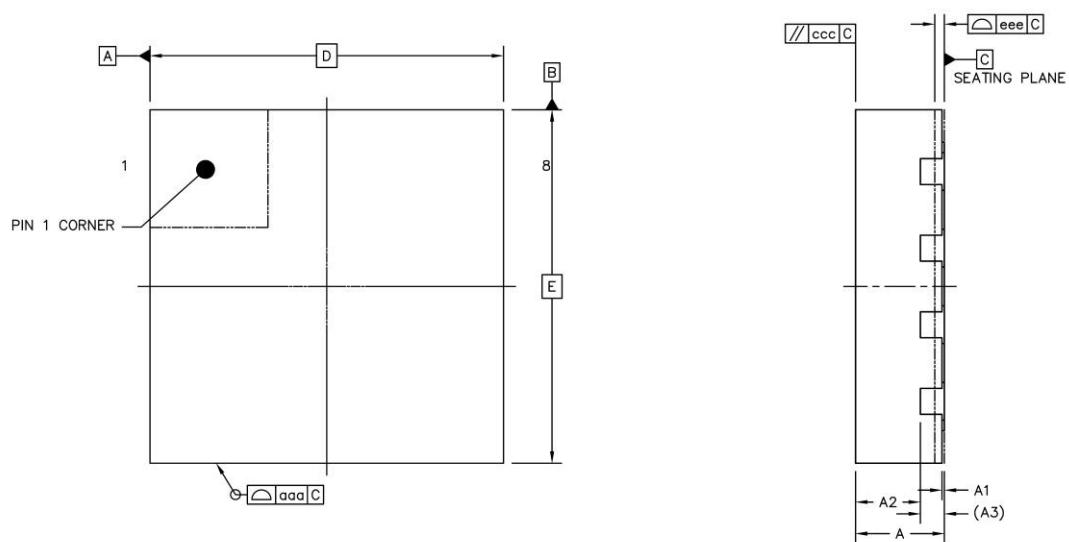
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Static Characteristics						
Drain-source breakdown voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	30			V
Zero gate voltage drain current	I_{DSS}	$V_{\text{DS}} = 30\text{V}, V_{\text{GS}} = 0\text{V}$			1	μA
Gate-body leakage current	I_{GSS}	$V_{\text{GS}} = \pm 20\text{V}, V_{\text{DS}} = 0\text{V}$			± 100	nA
Gate threshold voltage	$V_{\text{GS}(\text{th})}^{(4)}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	1.0	1.7	3.0	V
Drain-source on-resistance	$R_{\text{DS}(\text{on})}^{(4)}$	$V_{\text{GS}} = 10\text{V}, I_D = 12\text{A}$		8	12	$\text{m}\Omega$
		$V_{\text{GS}} = 4.5\text{V}, I_D = 10\text{A}$		12	18	
Forward transconductance	$g_{\text{FS}}^{(4)}$	$V_{\text{DS}} = 5\text{V}, I_D = 10\text{A}$	5	12		S
Dynamic characteristics⁽⁵⁾						
Input capacitance	C_{iss}	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		1277		pF
Output capacitance	C_{oss}			144		
Reverse transfer capacitance	C_{rss}			134		
Switching Characteristics⁽⁵⁾						
Total gate charge	Q_g	$V_{\text{DS}} = 15\text{V}, V_{\text{GS}} = 5\text{V}, I_D = 10\text{A}$		13.5		nC
Gate-source charge	Q_{gs}			5.6		
Gate-drain charge	Q_{gd}			3.7		
Turn-on delay time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 25\text{V}, V_{\text{GS}} = 10\text{V}, R_G = 63\Omega, R_L = 6.7\Omega, I_D = 1\text{A}$		31		ns
Turn-on rise time	t_r			22		
Turn-off delay time	$t_{\text{d}(\text{off})}$			105		
Turn-off fall time	t_f			82		
Diode Characteristics						
Continuous Source Current	I_s	$V_G = V_D = 0\text{V}$, Force Current			15	A
Pulsed Source Current	I_{SM}				45	
Diode Forward Voltage	$V_{\text{SD}}^{(4)}$	$V_{\text{GS}} = 0\text{V}, I_s = 10\text{A}, T_J = 25^\circ\text{C}$		0.85	1.2	V

Notes:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper
- 2.Pulse Test:Pulse Width < 10us, Duty Cycle < 0.5%.
- 3.The power dissipation is limited by 150°C junction temperature
- 4.Pulse Test : Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 0.5\%$.
- 5.Guaranteed by design, not subject to production testing.
- 6.The data is theoretically the same as I_D , in real applications , should be limited by total power dissipation.

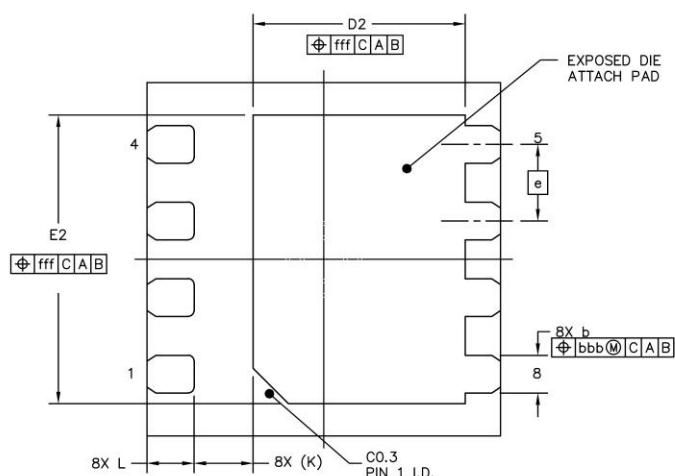
Typical Electrical and Thermal Characteristics



DFN3*3-8L Package Information


TOP VIEW

SIDE VIEW



BOTTOM VIEW

DFN3*3-8L Package Information

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.700	0.800	0.028	0.031
A1	0.000	0.050	0.000	0.002
A2	0.550TYP		0.022TYP	
A3	0.203REF		0.008REF	
b	0.270	0.370	0.011	0.015
D	3.000BSC		0.118BSC	
E	3.000BSC		0.118BSC	
e	0.650BSC		0.026BSC	
D2	1.700	1.900	0.067	0.075
E2	2.350	2.550	0.093	0.100
L	0.300	0.500	0.012	0.020
K	0.500REF		0.020REF	
aaa	0.100TYP		0.004TYP	
ccc	0.100TYP		0.004TYP	
eee	0.080TYP		0.003TYP	
bbb	0.100TYP		0.004TYP	
fff	0.100TYP		0.004TYP	