



**GP**  
**ELECTRONICS**

# GPM1K1NP02USE

## 20V N- and P-Channel MOSFET

### Product Summary

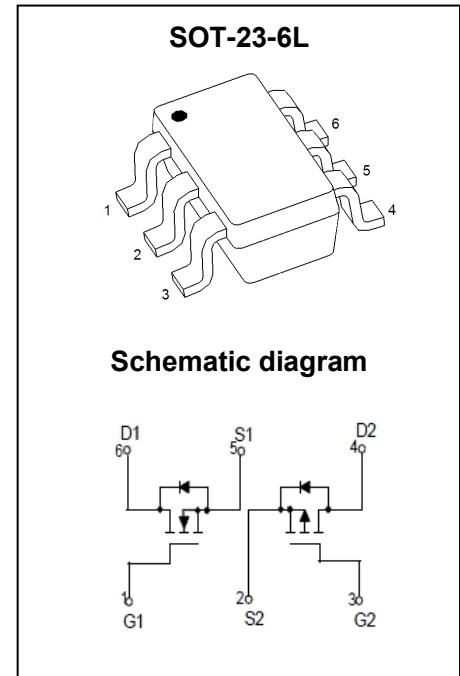
$V_{(BR)DSS}$	$R_{DS(on)TYP}$	$I_D$
20V	32m $\Omega$ @4.5V	3.5A
	50m $\Omega$ @2.5V	
-20V	85m $\Omega$ @-4.5V	-2.1A
	115m $\Omega$ @-2.5V	

### Feature

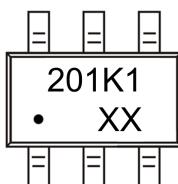
- Trench Technology Power MOSFET
- Low  $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance

### Application

- Power Switching Application



### MARKING:



201K1 = Device Code  
XX = Date Code

### ABSOLUTE MAXIMUM RATINGS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Value	Unit
Drain - Source Voltage	$V_{DS}$	20	-20	V
Gate - Source Voltage	$V_{GS}$	$\pm 10$	$\pm 10$	V
Continuous Drain Current <sup>1</sup>	$I_D$	3.5	-2.1	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	14	-8	A
Power Dissipation <sup>4</sup>	$P_D$	0.8	0.8	W
Thermal Resistance from Junction to Ambient <sup>5</sup>	$R_{\theta JA}$	156	156	$^\circ\text{C}/\text{W}$
Junction Temperature	$T_J$	150	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55~+150	-55~+150	$^\circ\text{C}$

**MOSFET ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$  unless otherwise noted)**
**NMOS:**

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	20			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 20\text{V}, V_{\text{GS}} = 0\text{V}$			1	$\mu\text{A}$
Gate - Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 10\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	0.6	1.0	1.2	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 4.5\text{V}, I_D = 3.6\text{A}$		32	43	$\text{m}\Omega$
		$V_{\text{GS}} = 2.5\text{V}, I_D = 3.1\text{A}$		50	68	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		252		pF
Output Capacitance	$C_{\text{oss}}$			52		
Reverse Transfer Capacitance	$C_{\text{rss}}$			45		
Gate Resistance	$R_g$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		2.8		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, I_D = 3.6\text{A}$		4		nC
Gate-source Charge	$Q_{\text{gs}}$			0.6		
Gate-drain Charge	$Q_{\text{gd}}$			1.6		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 10\text{V}, V_{\text{GS}} = 4.5\text{V}, R_L = 3.3\Omega$ $R_G = 6\Omega$		2.6		ns
Turn-on Rise Time	$t_r$			3.3		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			22		
Turn-off Fall Time	$t_f$			4		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = 0.94\text{A}$			1.2	V

**MOSFET ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$  unless otherwise noted)**
**PMOS:**

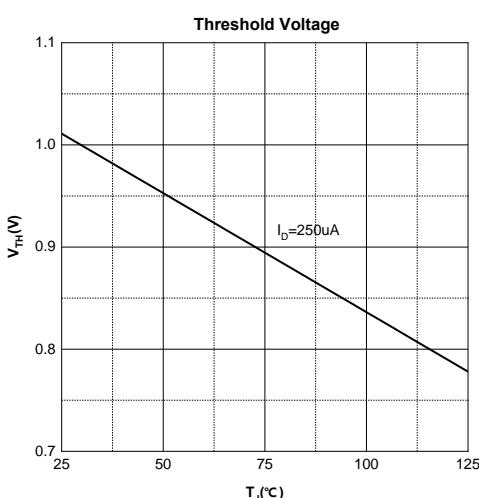
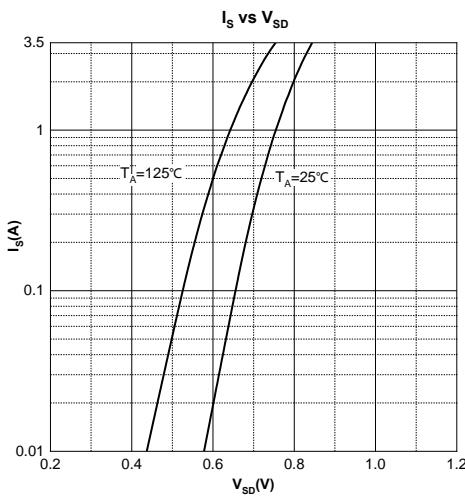
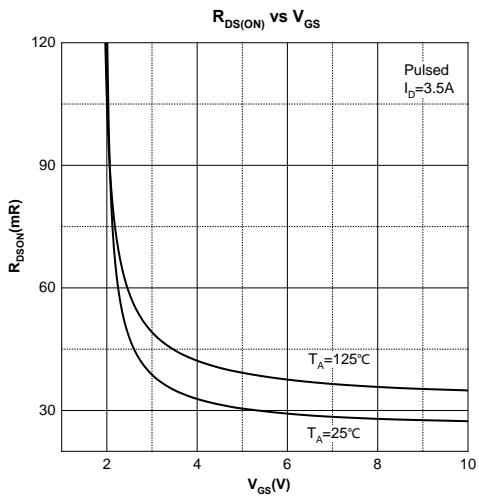
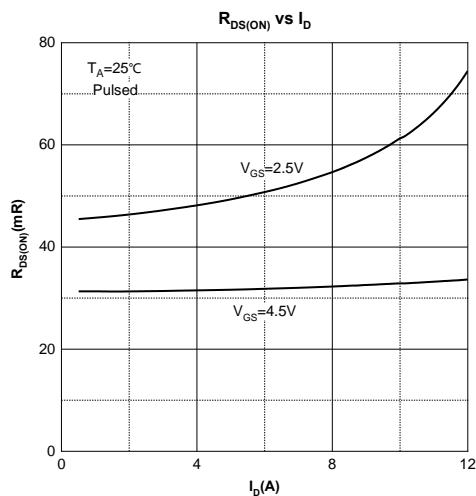
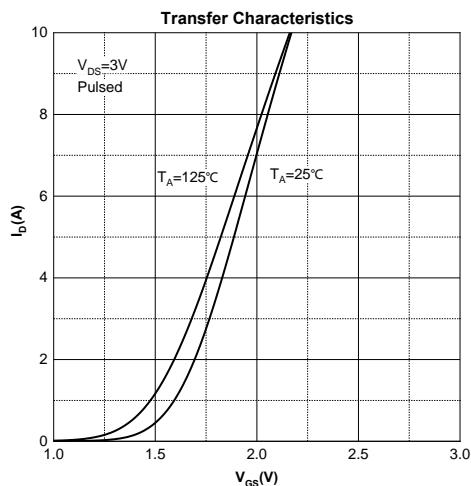
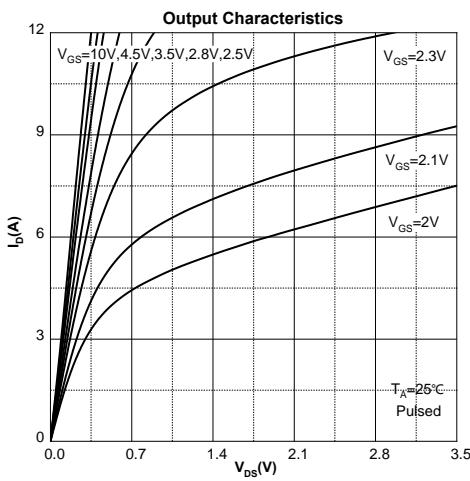
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Off Characteristics</b>						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-20			V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = -20\text{V}, V_{\text{GS}} = 0\text{V}$			-1	$\mu\text{A}$
Gate - Body Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 10\text{V}, V_{\text{DS}} = 0\text{V}$			$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-0.4	-0.7	-1.0	V
Drain-source On-resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = -10\text{V}, I_D = -2.8\text{A}$		85	111	$\text{m}\Omega$
		$V_{\text{GS}} = -4.5\text{V}, I_D = -2\text{A}$		115	150	
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}} = -20\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		394		$\text{pF}$
Output Capacitance	$C_{\text{oss}}$			49		
Reverse Transfer Capacitance	$C_{\text{rss}}$			40		
Gate Resistance	$R_g$	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = 0\text{V}, f = 1\text{MHz}$		30		$\Omega$
<b>Switching Characteristics</b>						
Total Gate Charge	$Q_g$	$V_{\text{DS}} = -10\text{V}, V_{\text{GS}} = -4.5\text{V}, I_D = -2.8\text{A}$		4.5		$\text{nC}$
Gate-source Charge	$Q_{\text{gs}}$			1.0		
Gate-drain Charge	$Q_{\text{gd}}$			1.1		
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = -10\text{V}, V_{\text{GS}} = -4.5\text{V}, R_L = 5\Omega, R_G = 3\Omega$		12		$\text{ns}$
Turn-on Rise Time	$t_r$			5.6		
Turn-off Delay Time	$t_{\text{d}(\text{off})}$			23		
Turn-off Fall Time	$t_f$			9		
<b>Source - Drain Diode Characteristics</b>						
Diode Forward Voltage <sup>3</sup>	$V_{\text{SD}}$	$V_{\text{GS}} = 0\text{V}, I_s = -0.7\text{A}$			-1.2	V

Notes :

- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width  $\leq 10\mu\text{s}$ , duty cycle  $\leq 1\%$ .
- 3.Pulse Test : Pulse Width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
- 4.The power dissipation  $P_D$  is limited by  $T_{J(\text{MAX})} = 150^\circ\text{C}$ .And device mounted on a large heatsink
- 5.Device mounted on 1in<sup>2</sup> FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$ .

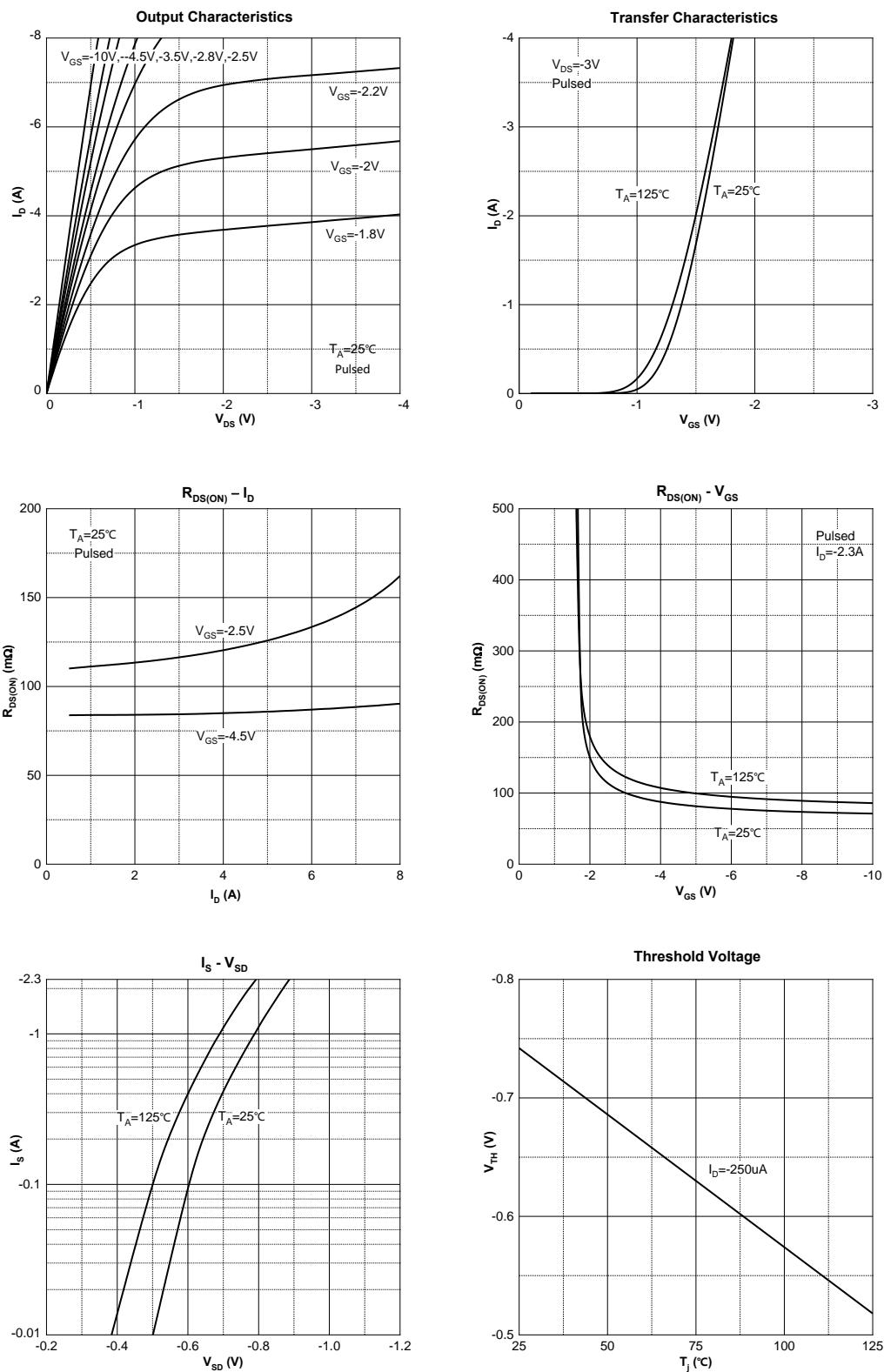
## Typical Characteristics

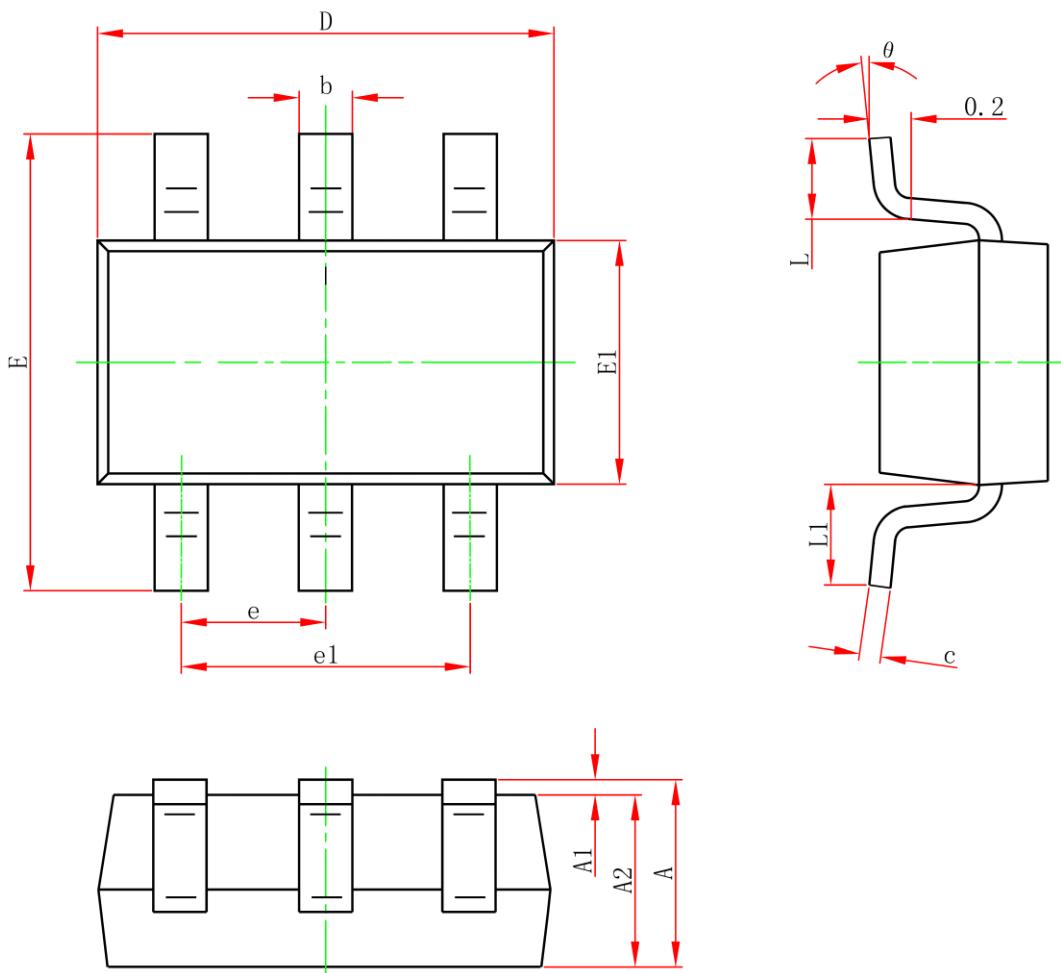
NMOS:



## Typical Characteristics

**PMOS:**



**SOT-23-6L Package Information**


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0	0.150	0.000	0.006
A2	1.050	1.250	0.041	0.049
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	2.650	2.950	0.104	0.116
E1	1.500	1.700	0.059	0.067
e	0.950TYP		0.037TYP	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
L1	0.600REF		0.024REF	
theta	0°	8°	0°	8°