



GP
ELECTRONICS

GP17P10D

100V P-Channel MOSFET

Product Summary

$V_{(BR)DSS}$	$R_{DS(on)}TYP$	I_D
-100V	100mΩ@-10V	-17A

Feature

- Trench Technology Power MOSFET
- Low $R_{DS(ON)}$
- Low Gate Charge
- Low Gate Resistance
- 100% ΔV_{DS} Tested

Application

- Power Switching Application

MARKING:

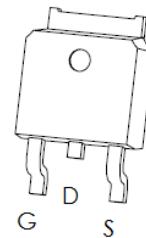


U17P10 = Device Code

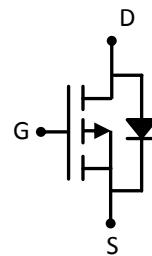
XX = Date Code

Solid Dot = Green Indicator

TO-252-2L



Schematic diagram



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain - Source Voltage	V_{DS}	-100	V
Gate - Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	-17	A
	I_D	-11	A
Pulsed Drain Current ²	I_{DM}	-68	A
Single Pulsed Avalanche Current ³	I_{AS}	-23	A
Single Pulsed Avalanche Energy ³	E_{AS}	132	mJ
Power Dissipation ⁵	P_D	132	W
Thermal Resistance from Junction to Ambient ⁶	$R_{\theta JA}$	75	°C/W
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.78	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~+150	°C

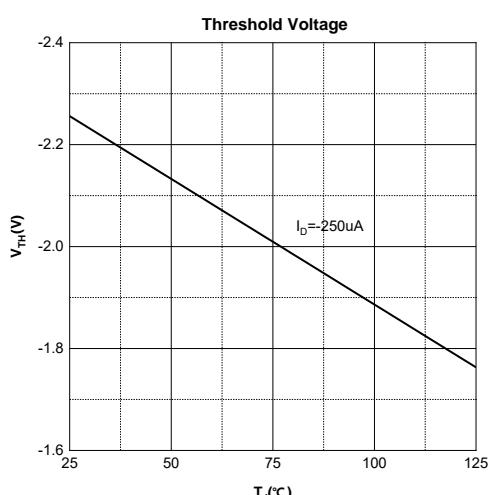
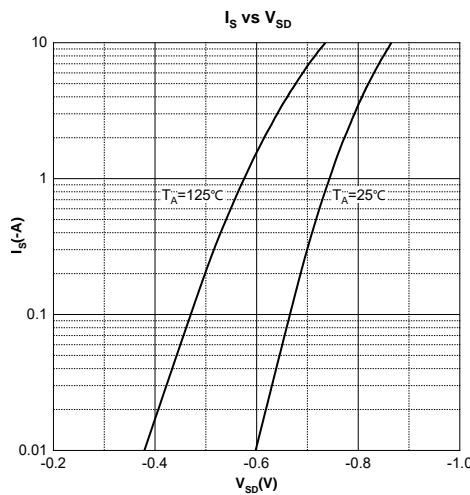
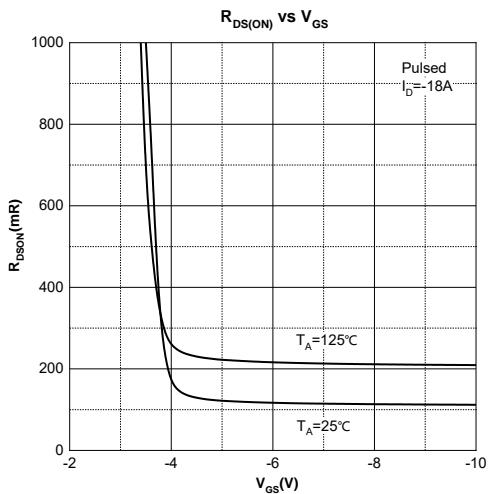
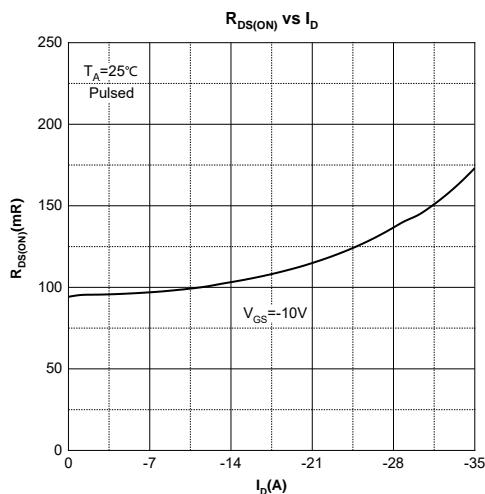
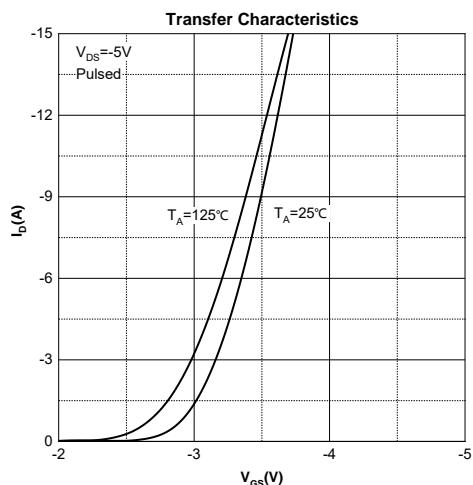
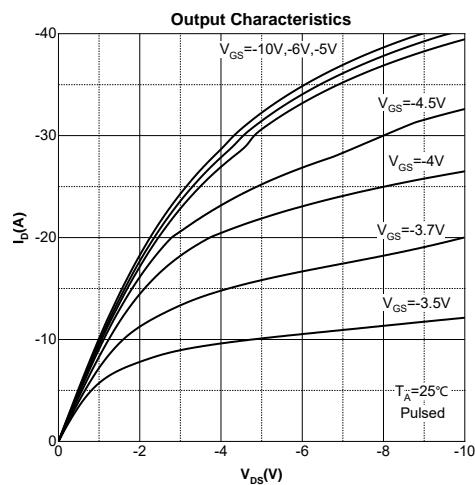
MOSFET ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

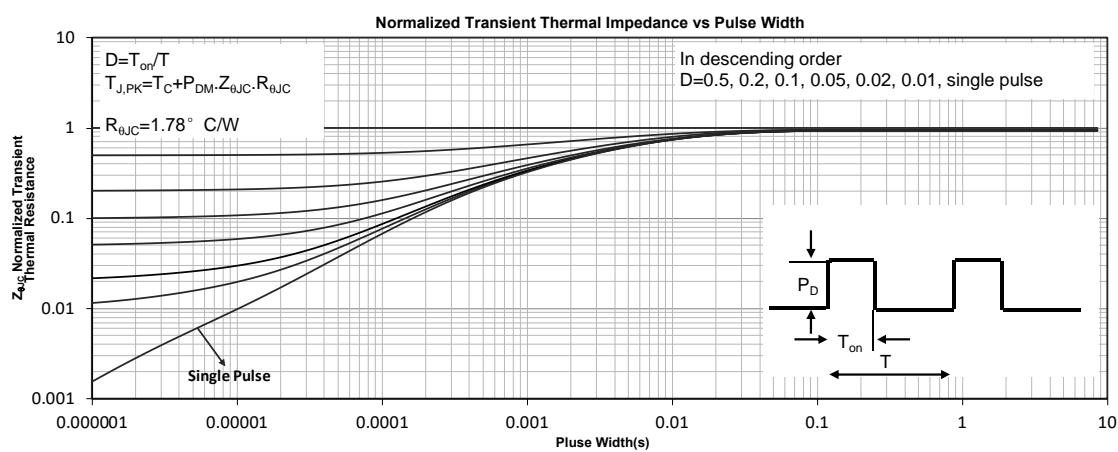
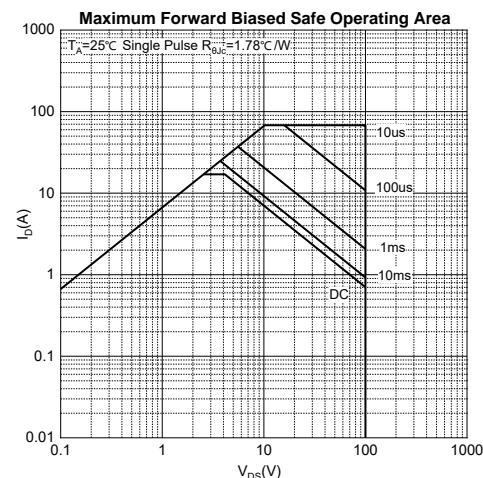
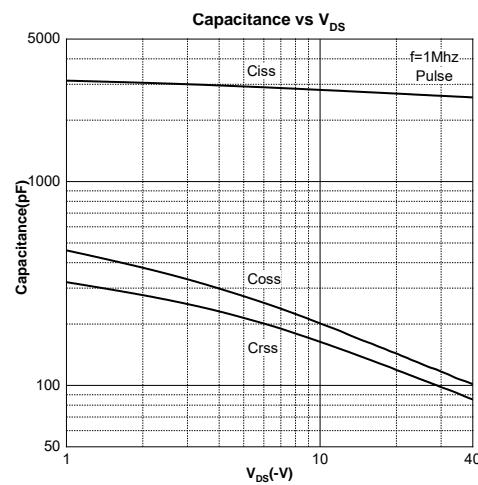
Parameter	Symbol	Test Condition	Min	Type	Max	Unit
Off Characteristics						
Drain - Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{GS} = 0V, I_D = -250\mu\text{A}$	-100			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -80V, V_{GS} = 0V$			-1	μA
Gate - Body Leakage Current	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$			± 100	nA
On Characteristics⁴						
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = -250\mu\text{A}$	-1	-2.2	-3	V
Drain-source On-resistance	$R_{DS(\text{on})}$	$V_{GS} = -10V, I_D = -10\text{A}$		100	127	$\text{m}\Omega$
Dynamic Characteristics						
Input Capacitance	C_{iss}	$V_{DS} = -45V, V_{GS} = 0V, f = 1\text{MHz}$		2564		pF
Output Capacitance	C_{oss}			95		
Reverse Transfer Capacitance	C_{rss}			81		
Gate Resistance	R_g	$V_{DS} = 0V, V_{GS} = 0V, f = 1\text{MHz}$		4.2		Ω
Switching Characteristics						
Total Gate Charge	Q_g	$V_{DS} = -50V, V_{GS} = -10V, I_D = -10\text{A}$		48		nC
Gate-source Charge	Q_{gs}			8.3		
Gate-drain Charge	Q_{gd}			12		
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = -50V, V_{GS} = -10V, R_G = 3\Omega, I_D = -10\text{A}$		12		ns
Turn-on Rise Time	t_r			29		
Turn-off Delay Time	$t_{d(off)}$			81		
Turn-off Fall Time	t_f			60		
Source - Drain Diode Characteristics						
Diode Forward Voltage ⁴	V_{SD}	$V_{GS} = 0V, I_S = -5\text{A}$			-1.2	V

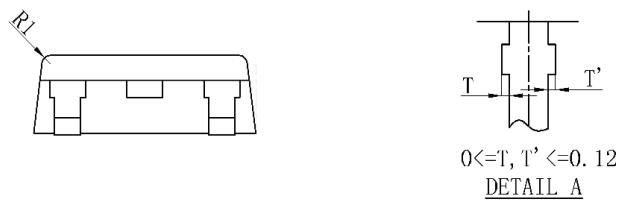
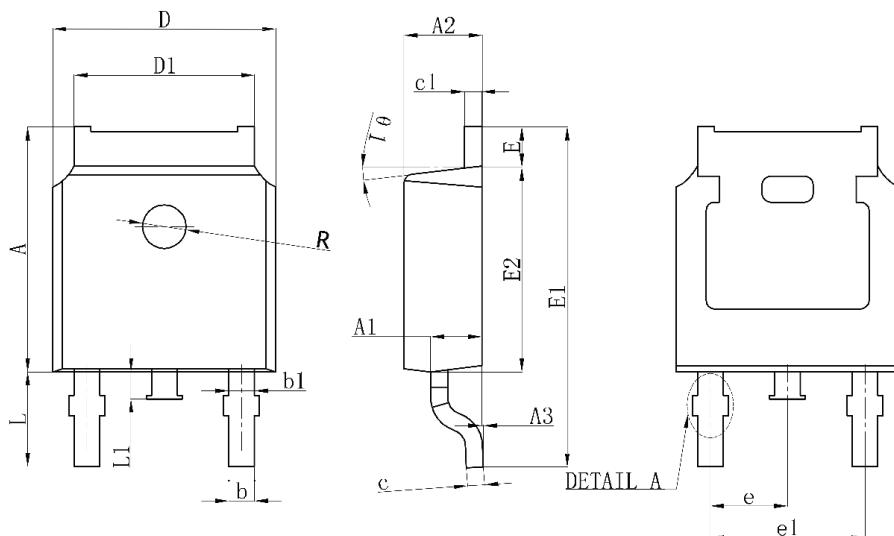
Notes :

- 1.The maximum current rating is limited by package.And device mounted on a large heatsink
- 2.Pulse Test : Pulse Width $\leq 10\mu\text{s}$, duty cycle $\leq 1\%$.
- 3.E_{AS} condition: $V_{DD} = -50V, V_{GS} = -10V, L = 0.5\text{mH}, R_G = 25\Omega$ Starting $T_J = 25^\circ\text{C}$.
- 4.Pulse Test : Pulse Width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- 5.The power dissipation P_D is limited by $T_{J(\text{MAX})} = 150^\circ\text{C}$.And device mounted on a large heatsink
- 6.Device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A = 25^\circ\text{C}$.

Typical Characteristics





TO-252-2L Package Information


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	7.050	7.150	0.278	0.281
A1	0.960	1.060	0.038	0.042
A2	2.200	2.400	0.087	0.094
A3	0.000	0.100	0.000	0.004
b	0.760REF		0.030REF	
b1	1.000REF		0.039REF	
c	0.508REF		0.020REF	
c1	0.508REF		0.020REF	
D	6.550	6.650	0.258	0.262
D1	5.100	5.460	0.201	0.215
E	0.950	1.050	0.037	0.041
E1	9.700	10.400	0.382	0.409
E2	6.000	6.200	0.236	0.244
e	2.286BSC		0.090BSC	
e1	4.572REF		0.180REF	
L	2.650	2.950	0.104	0.116
L1	0.700	0.900	0.028	0.035
θ1	7°REF		7°REF	
R	1.300REF		0.051REF	
R1	0.250REF		0.010REF	